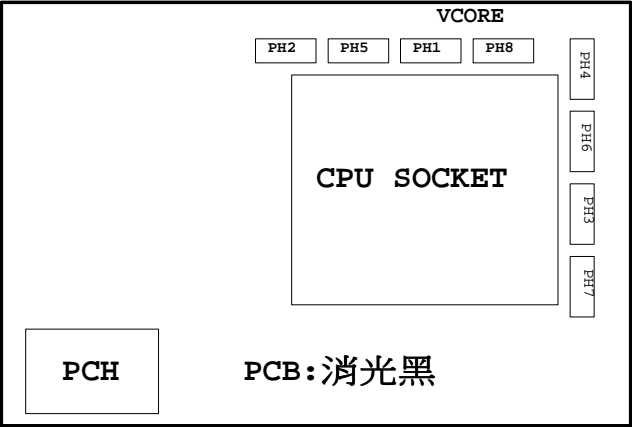


SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE
10	PCH_RGB,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*8 SLOT
16	PCI EXPRESS*16 SWITCH
17	PCI EXPRESS*4 SLOT
18	PCI EXPRESS*1 1,2,3 SLOT
19	ITE8892E
20	PCI SLOT
21	ALC1150 CODEC
22	REAR AUDIO JACK
23	ITE8620
24	COM/KB_MS/R_USB/PROHOT/USB PROTECT
25	IR3563B
26	IR3598 VCORE Phase 1, 4, 2, 5
27	DDR POWER

SHEET TITLE

28	DISCRETE POWER,NCT3933
29	DUAL BIOS
30	FP,F_USB,USB PWR,BZ
31	ATX POWER CONNECTOR
32	H/W MONITOR,FAN CTRL
33	DVI
34	HDMI_USB30
35	ARTHEROS E2201
36	M.2_SATA_EXPRESS
37	TABLE LIST
38	IR3598 VCORE Phase 6, 8, 3, 7
39	IR3570 _DDR PWM
40	IR 3598-DDR_2-Phase
41	Marvell 9172
42	RST, PWR, CLR_CMOS



MS

KB

RGB

DVI

USB3

USB3

HDMI

USB

USB

USB

USB

USB3

USB3

USB3

VCORE

PH2

PH5

PH1

PH8

PH4

PH6

PH3

PH7

PCB: 消光黑

PCH

Gigabyte Technology

Cover Sheet

Z97X-Gaming 7

Rev 1.0

Title

Document Number

Date: Thursday, March 13, 2014

Size Custom

Sheet 1 of 41

**Model Name:** GA-Z97X-GAMING 7

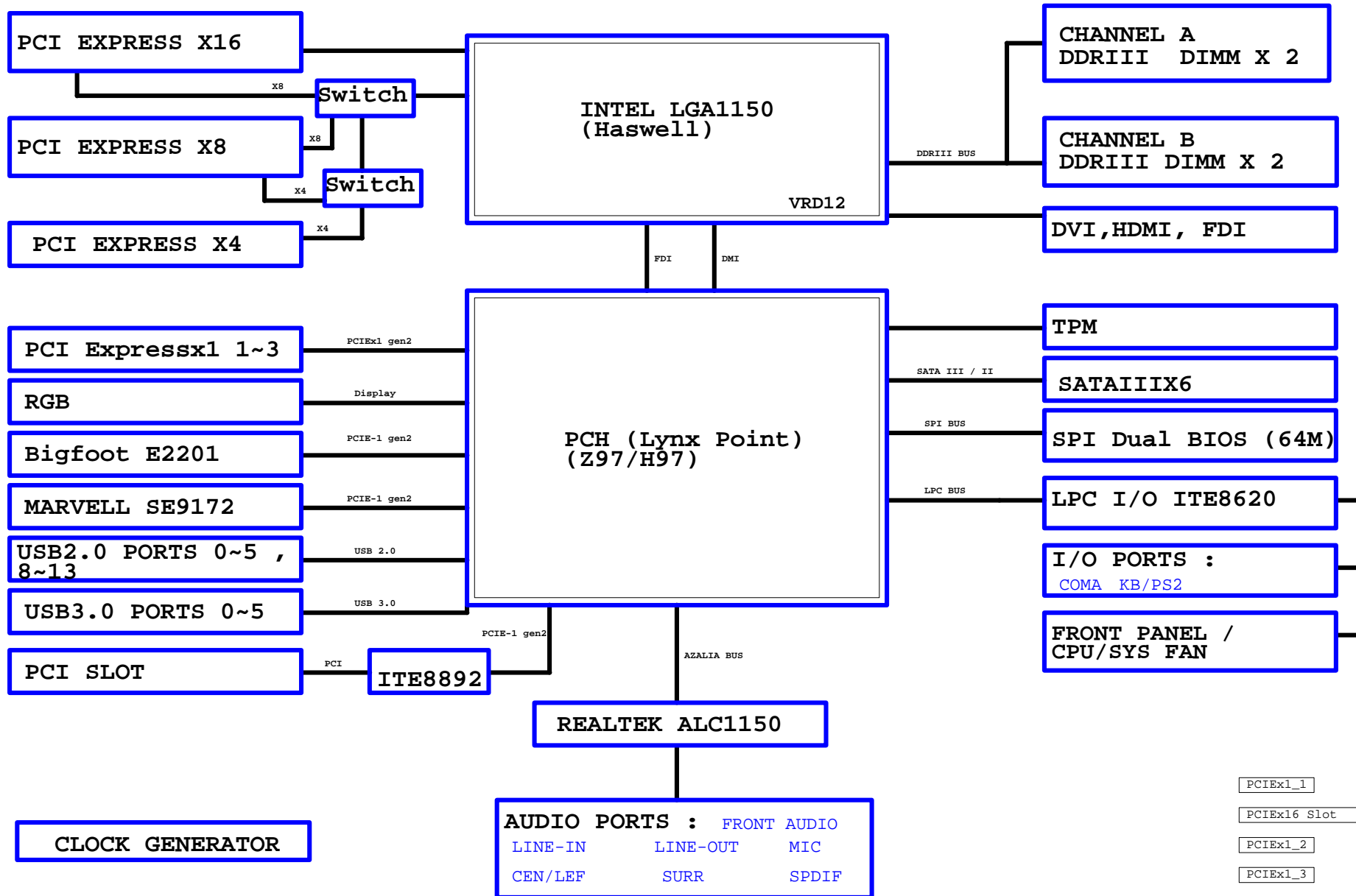
## Component value change history

[illegible]

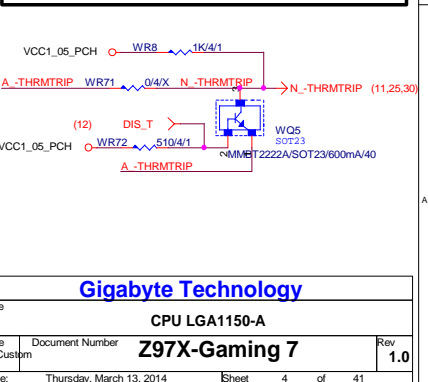
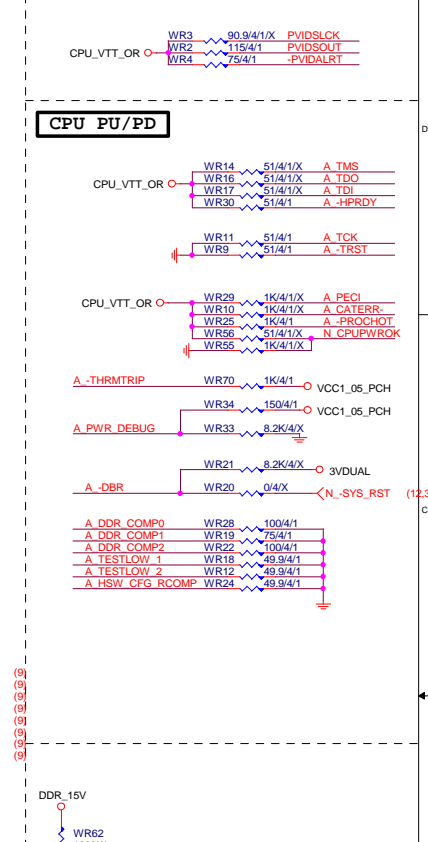
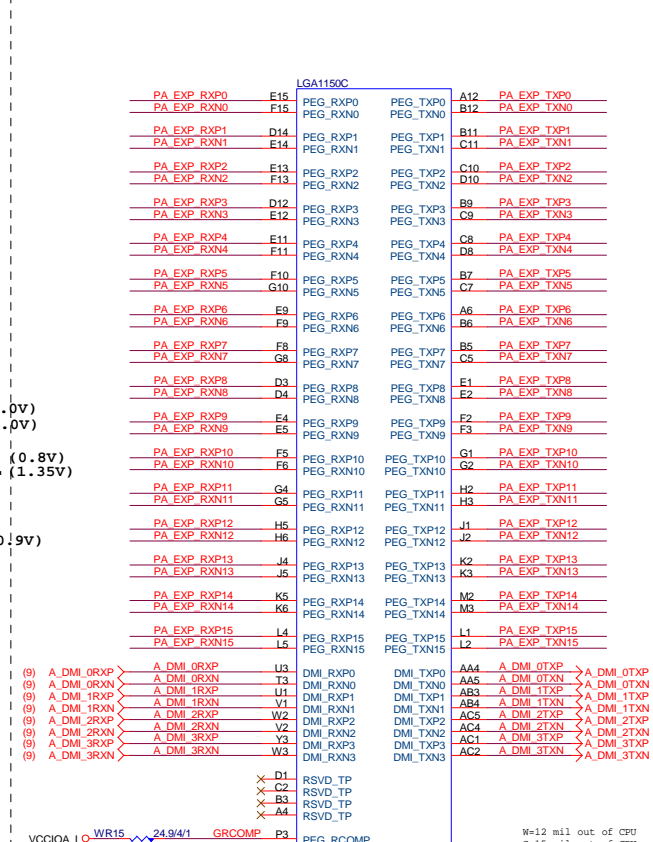
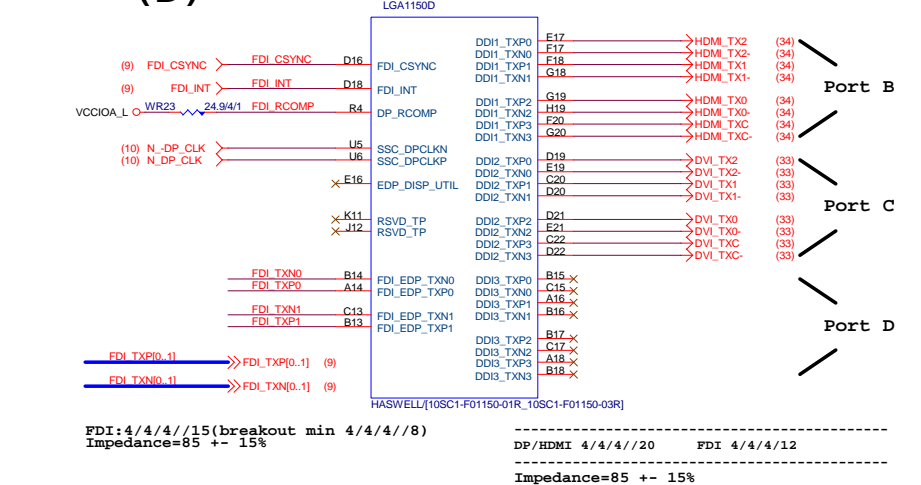
## Circuit or PCB layout change

[illegible]

# BLOCK DIAGRAM



- PCIEx1\_1
- PCIEx16 Slot
- PCIEx1\_2
- PCIEx1\_3
- PCIEx8
- PCI Slot
- PCIEx4



(A)

(7) -SRASA ← -SRASA AU12

(7) -SWEA ← -SWEA AU11

RSVD

HASWELL/I10SC1-F01150-01R I10SC1-F01150-03R)

**LGA1150 (B)**

(8) -SRASB ← -SWEB AK16 DDR1\_RAS\*  
(8) -SWEB ← -SWEB AK16 DDR1\_WE\*

(7) VREF\_DQA ← AB39 DDR\_VREF\_DQ0  
(8) VREF\_DQB ← AB40 DDR\_VREF\_DQ1

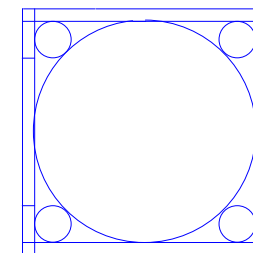


HASWELL/10SC1-F01150-01R 10SC1-F01150-03R

DDR1_DQS0	AJ7	MDBS5
DDR1_DQS1	AJ6	MDBS8
DDR1_DQS2	AE7	MDBS2
DDR1_DQS3	AF35	MD60
DDR1_DQS_P1	AN33	DSB81
DDR1_DQS_P2	AN23	DSB2
DDR1_DQS_P3	AP38	DSB3
DDR1_DQS_P4	AN12	DSB4
DDR1_DQS_P5	AP8	DSB5
DDR1_DQS_P6	AL7	DSB6
DDR1_DQS_P7	AE7	DSB7
DDR1_DQS_P8	AN25	DSB8
DDR1_DQS_N0	AF34	DSB81
DDR1_DQS_N1	AN33	DSB2
DDR1_DQS_N2	AN29	DSB3
DDR1_DQS_N3	AN13	DSB4
DDR1_DQS_N4	AR8	DSB5
DDR1_DQS_N5	AM8	DSB6
DDR1_DQS_N6	AG6	DSB7
DDR1_DQS_N8	AN26	DSB8

**LGA1150 (CR)**

U.S. AIR FORCE/115X/BKNI/[12KRC-0F0001-61R\_12KRC-0F0001-62R]



## DDR BUS

(7) MODT\_A[0..3]  $\leftrightarrow$  MODT\_A[0..3]  
(8) MODT\_B[0..3]  $\leftrightarrow$  MODT\_B[0..3]

(7) MDA[0..63]  $\leftrightarrow$  MDA[0..63]  
(8) MDB[0..63]  $\leftrightarrow$  MDB[0..63]

(7) DQSA[0..7]  $\leftrightarrow$  DQSA[0..7]  
(8) -DQSA[0..7]  $\leftrightarrow$  -DQSA[0..7]

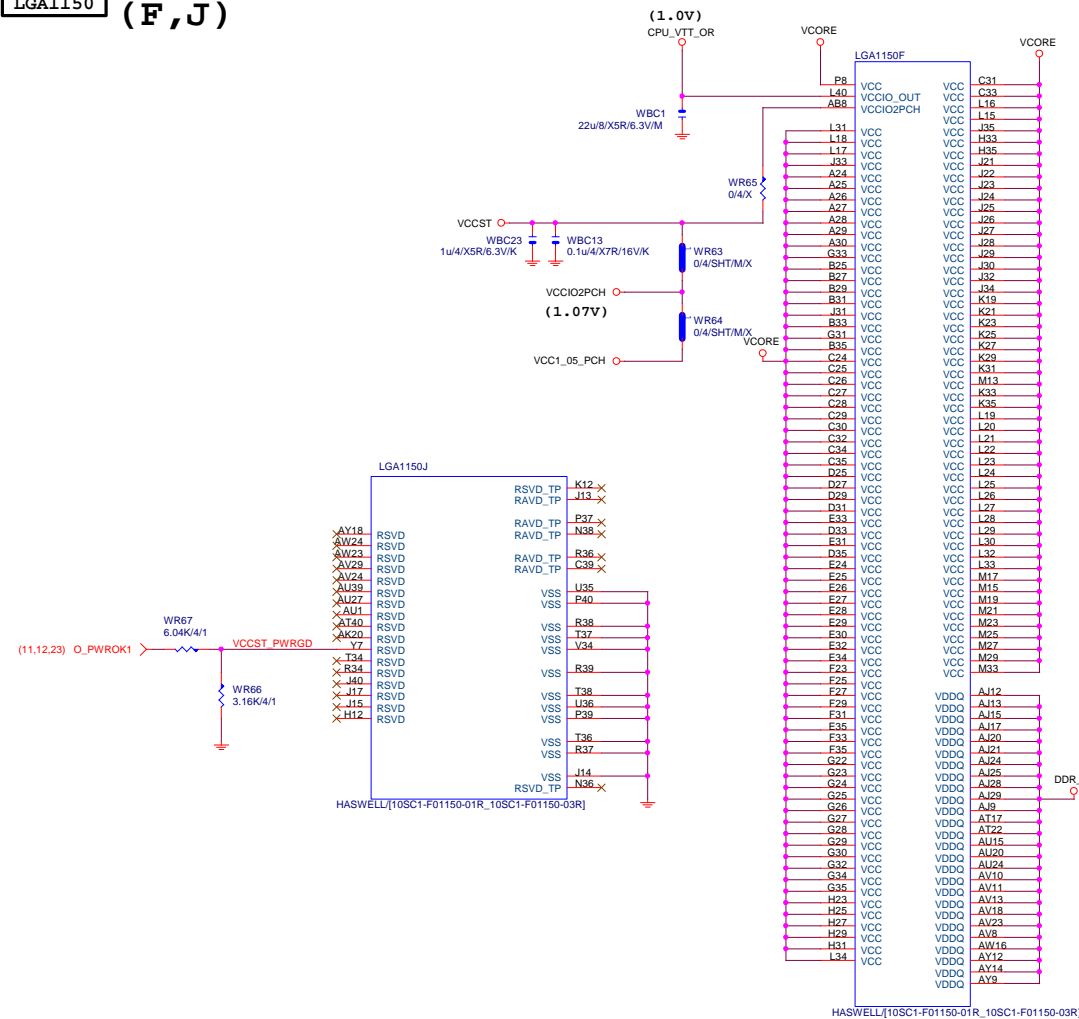
(7) MAAA[0..15]  $\leftrightarrow$  MAAA[0..15]  
(8) MAAB[0..15]  $\leftrightarrow$  MAAB[0..15]

(7) DQSB[0..7]  $\leftrightarrow$  DQSB[0..7]  
(8) -DQSB[0..7]  $\leftrightarrow$  -DQSB[0..7]

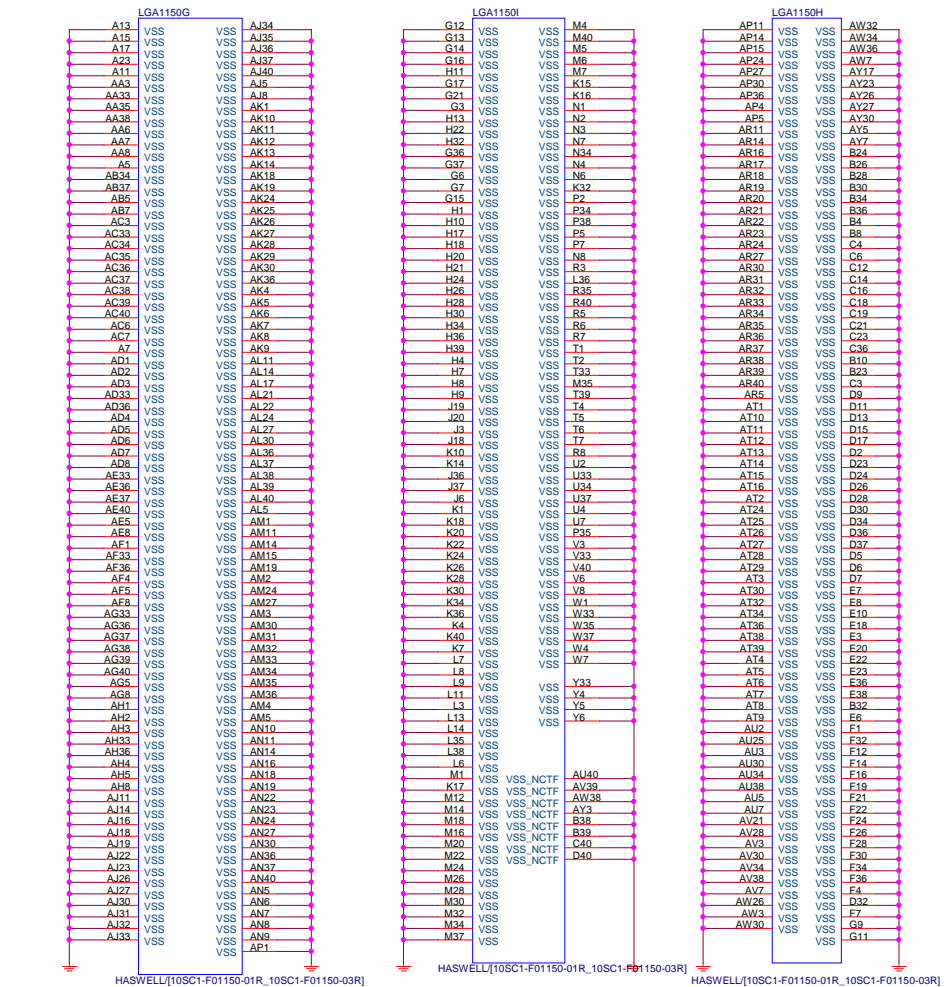
## Gigabyte Technology

Title			
CPU LGA1150-B			
Size	Document Number		Rev
Custom	Z97X-Gaming 7		1.0
Date:	Thursday, March 13, 2014	Sheet	5 of 41

**LGA1150 (F,J)**

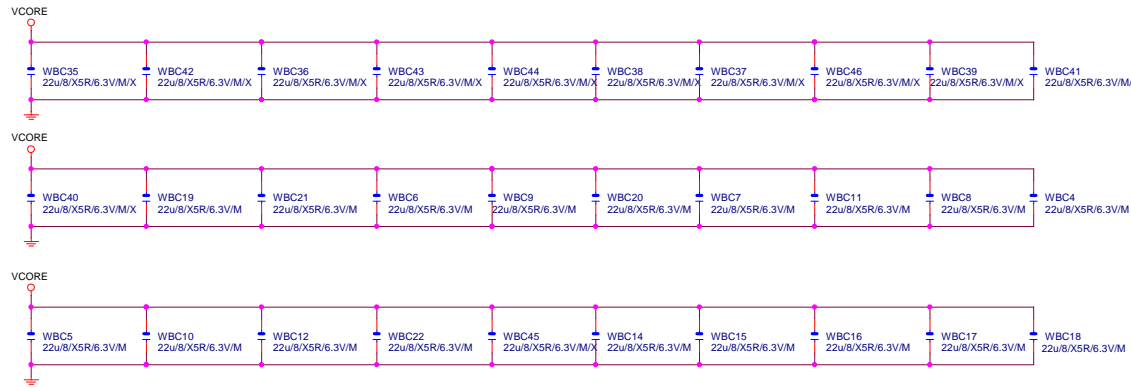


LGA1150 (G,H,I)



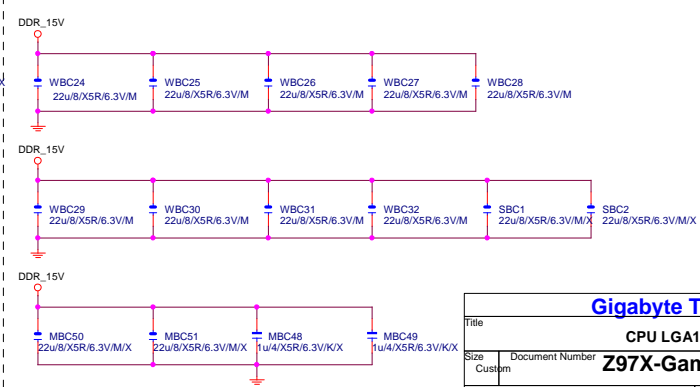
## VCore CAP

(X30)



DDR CAP

(X15)



## Gigabyte Technology

Title			
CPU LGA1150-C			
Size	Document Number	Rev	
Custom	Z97X-Gaming 7	1.0	
Date:	Thursday, March 13, 2014	Sheet	6 of 41



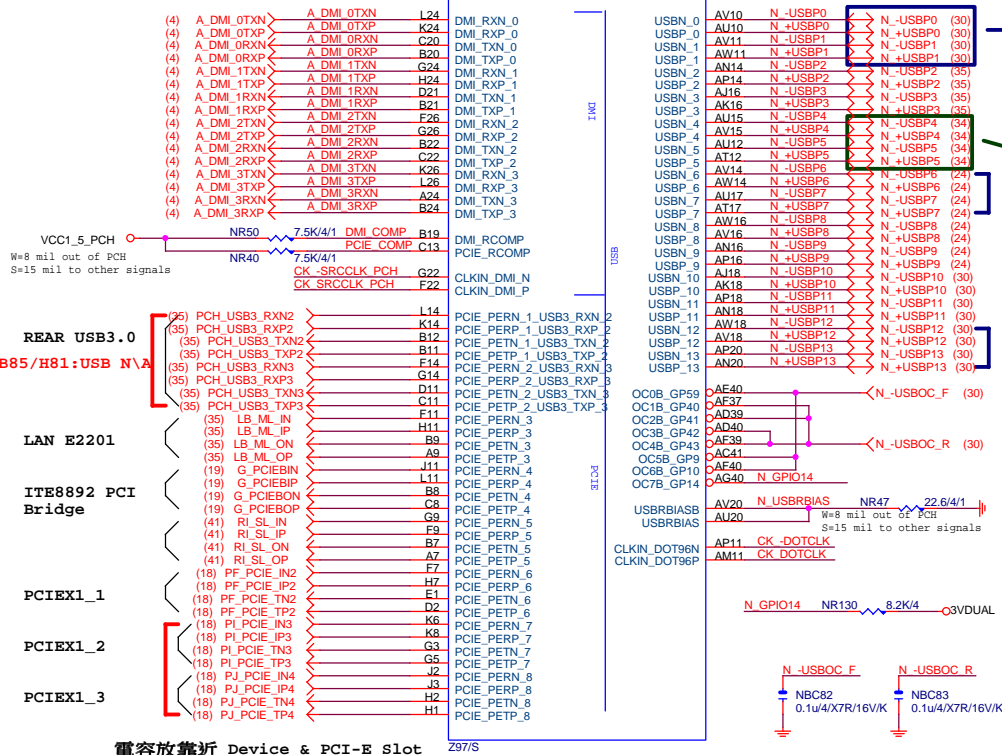






DMI:12/4/4/4/12(breakout min 8/4/4/4/8)  
Impedance=85 +- 17.5%

USB2.0 : 12/5/7/5/12 (breakout min 8/4/4/4/8)  
Impedance=85 +- 15%

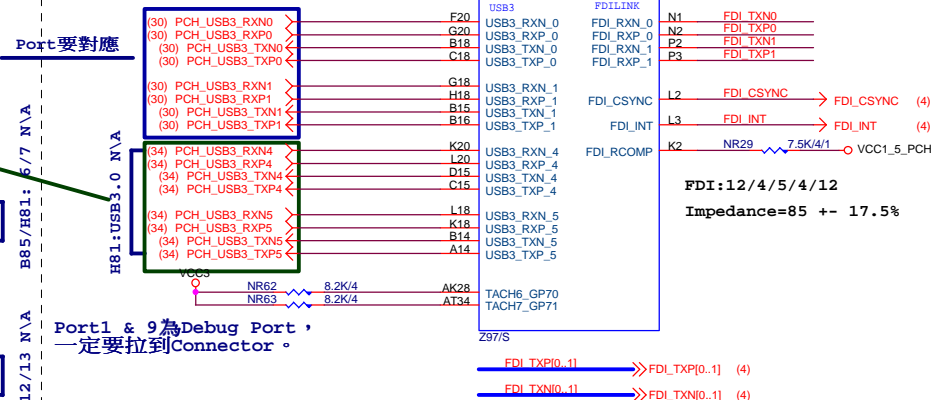


電容放靠近 Device & PCI-E Slot

PCH PCIe .DMI 4/4/4//15 Impedance=85 +- 15%

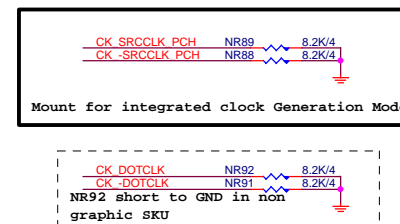
usb2.0 5/7/5//12  
usb3.0 5/7/5//20      Impedance=85 +- 15%

(30) PCH 11

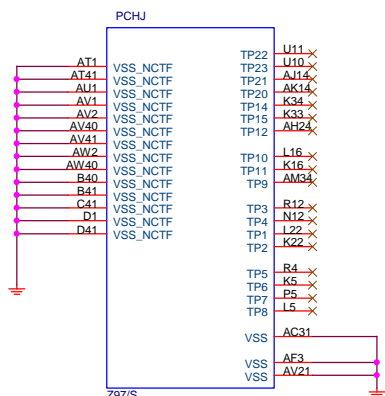


Port1 & 9為Debug Port，一定要拉到Connector。

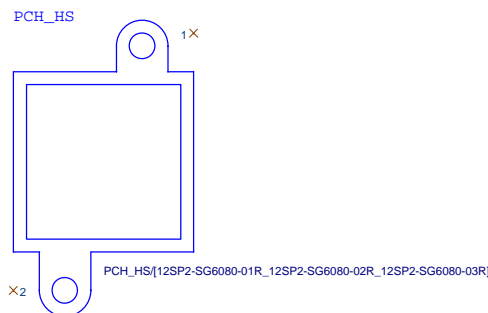
USB3.0:20/5/7/5/20 (breakout min  
8/4/4/4/8) ; ONLY 3 VIAS  
Impedance=85 +- 17.5%  
Back Panel < 10000 MILS  
Front Panel < 6000 MILS



PCHJ



PCH H/S



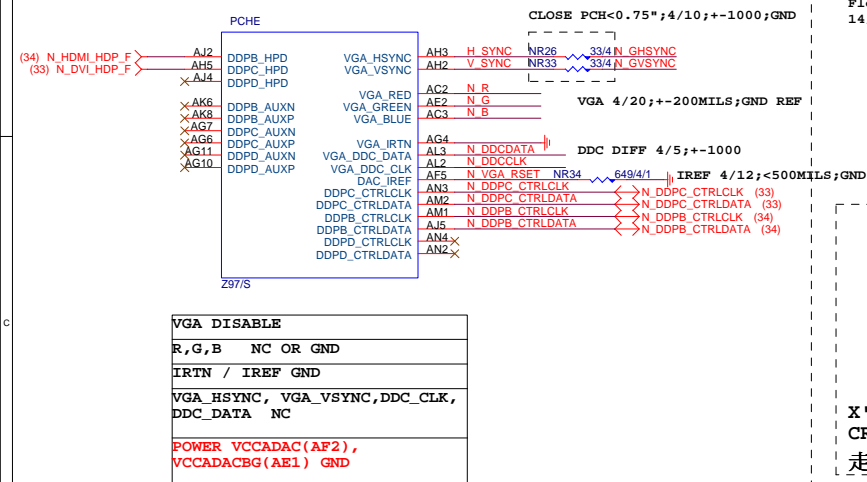
## USB TABLE

```
OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)
```

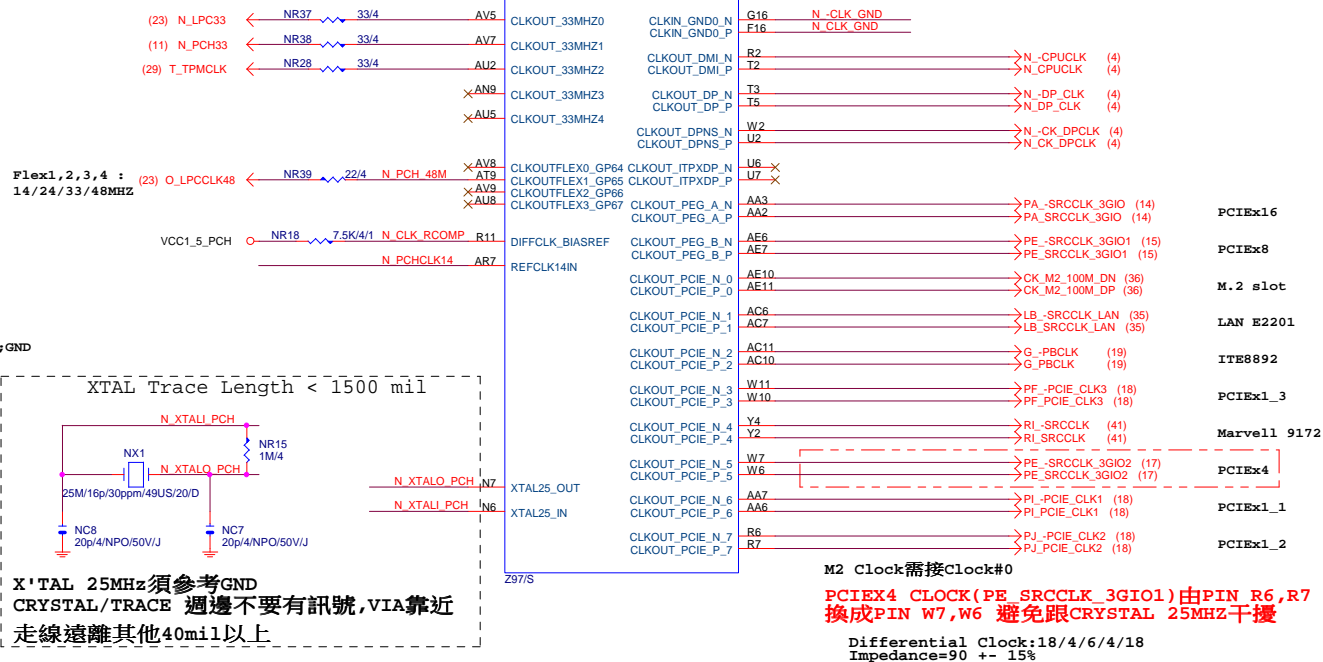
USB Usage & OC# Configure			
OC0#	USB0,1	F_USB30	FUSEVCC_F1_F2
OC1#	USB2,3	USB30_LAN	FUSEVCC_R7_R8
OC2#	USB4,5	HDMI & R_USB3	FUSEVCC_R1_R2
OC3#	USB6,7	4 Ports_R_USB (Up)	FUSEVCC_R5_R6
OC4#	USB8,9	4 Ports_R_USB (Down)	FUSEVCC_R3_R4
OC5#	USB10,11	F_USB2	FUSEVCC_F5_F6
OC6#	USB12,13	F_USB1	FUSEVCC_F3_F4
OC7#	Not Use		

<h1 style="text-align: center;">Gigabyte Technology</h1>				
Title <span style="float: right;">PCH FDI,DMI,USB ,PCIE</span>				
Size Custom	Document Number <span style="float: right;"><b>Z97X-Gaming 7</b></span>			Rev 1.
Date:	Thursday, March 13, 2014	Sheet	9 of 41	

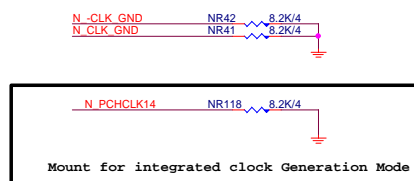
**PCH (E)**



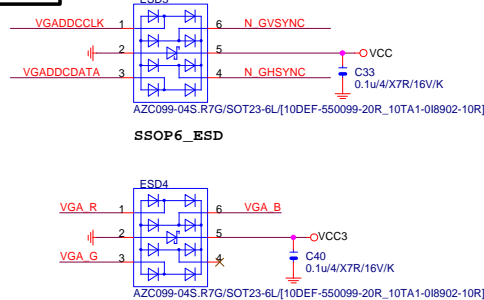
**PCH (G)**



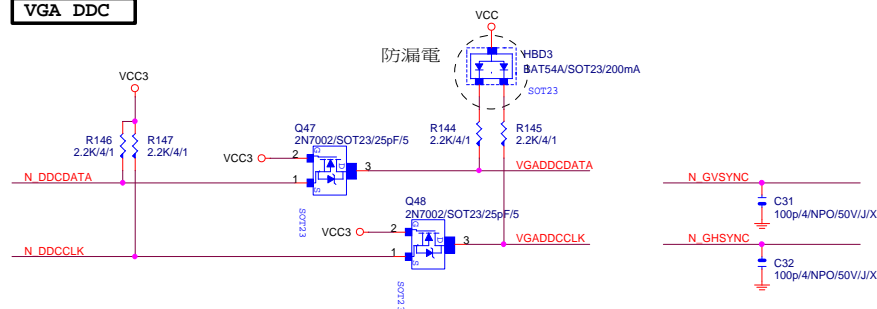
PCH CLK PD



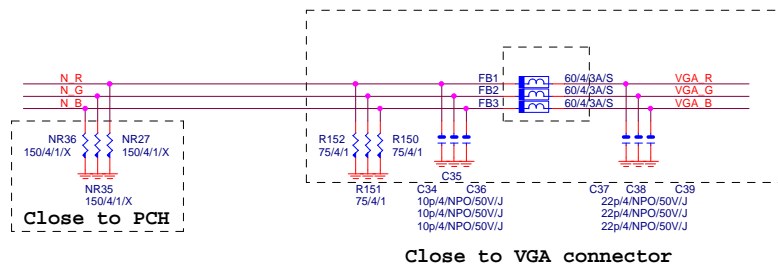
## VGA ESD



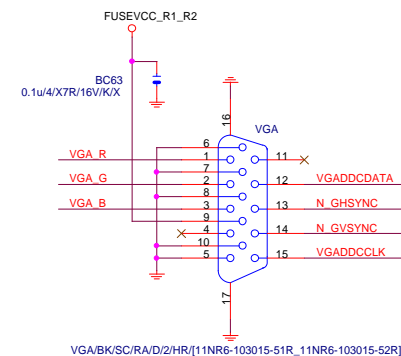
## VGA DDC



## VGA DDC



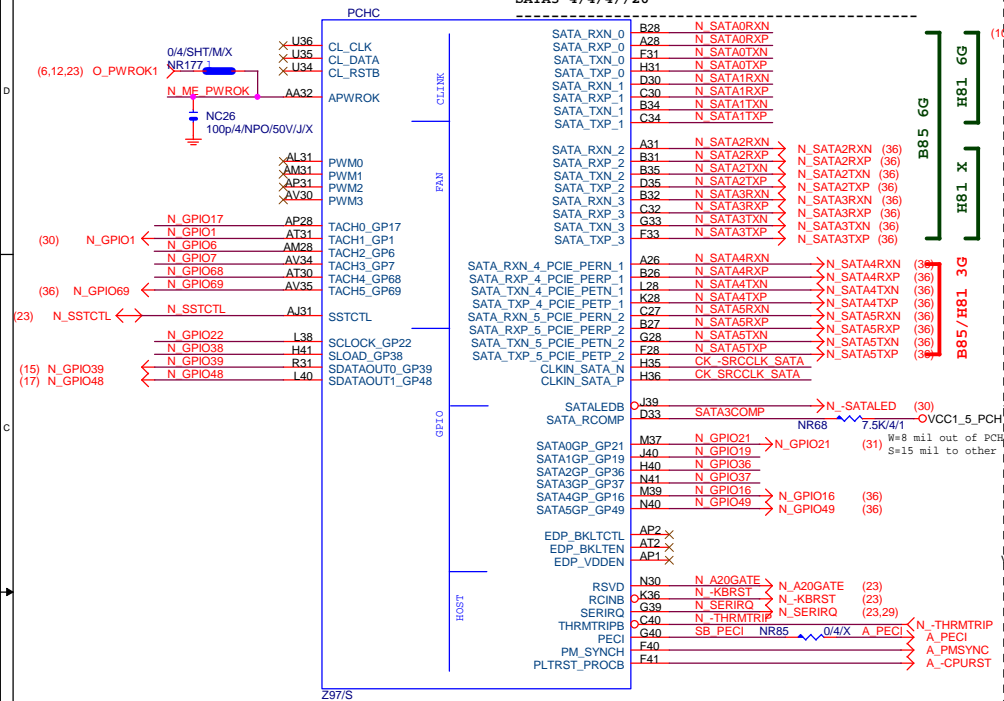
## VGA CONNECTOR



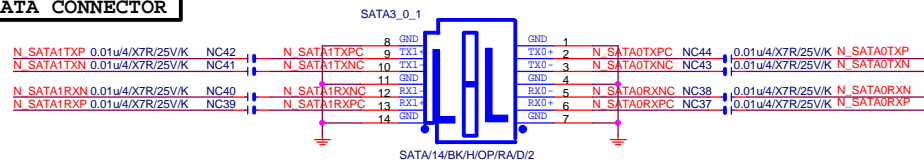
**PCH (C)**

SATA3 : 20/4/4/4/20 (breakout min 8/4/4/4/8)  
Impedance=85 +- 17.5%

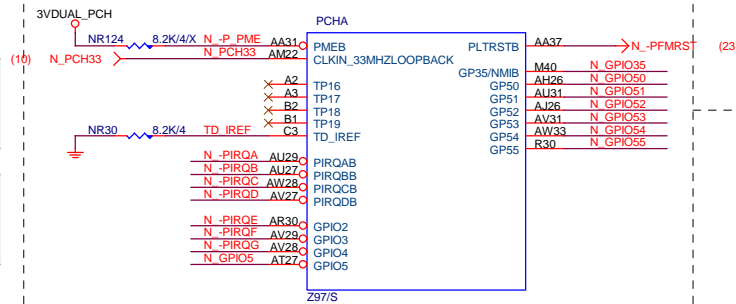
```
SATA2 4/4/4//15
SATA3 4/4/4//20
```



## SATA CONNECTOR



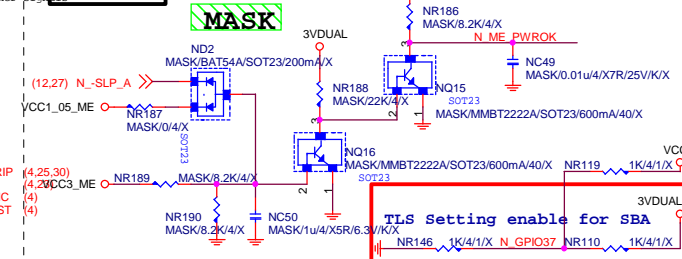
**PCH (A)**



```
Default int pull up on GP51,  
Default SPI boot devices
```

BOOT DEVICE	GP51	GP19
LPC	0	0
SPI	float	float

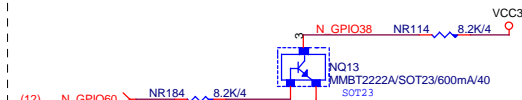
ME PWROK



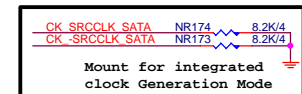
GPIO38 Ctrl

MFG Mode

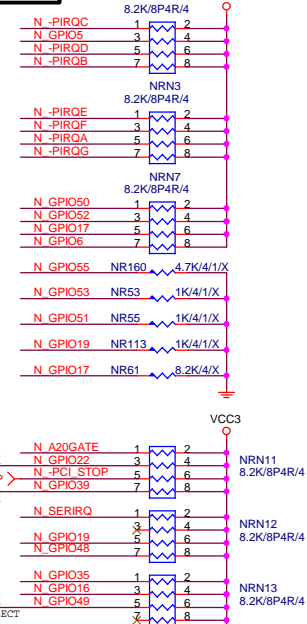
```
N_GPIO38 : Lo --> Enable
           Hi --> Disable
```



PCH CLK PD



PCH PU/PD



```

-| N_GPIO55:A16 SWAP OVERRIDE

```

```
| N_GPIO53:DMI AC COUPLING
```

```
| N_GPIO22:PCH CONFIG
```

(12) N-PCl STC

NR157 1K/4/1/

1. **Introduction**

② 2013 年 12 月 31 日, 甲公司应计提坏账准备的金额为 100 万元。

**Abstract**

N GPIO49:PCIE/MSATA MUX SE

soft		
------	--	--

strap	GP16	GP49
-------	------	------

0	pci1	pci2
---	------	------

1	sata4	sata5
---	-------	-------

1

NR84 1K/4/1/

NR66 1K/4/1/

—

1. *Journal of the American Medical Association*, 1997; 278: 1039-1044.

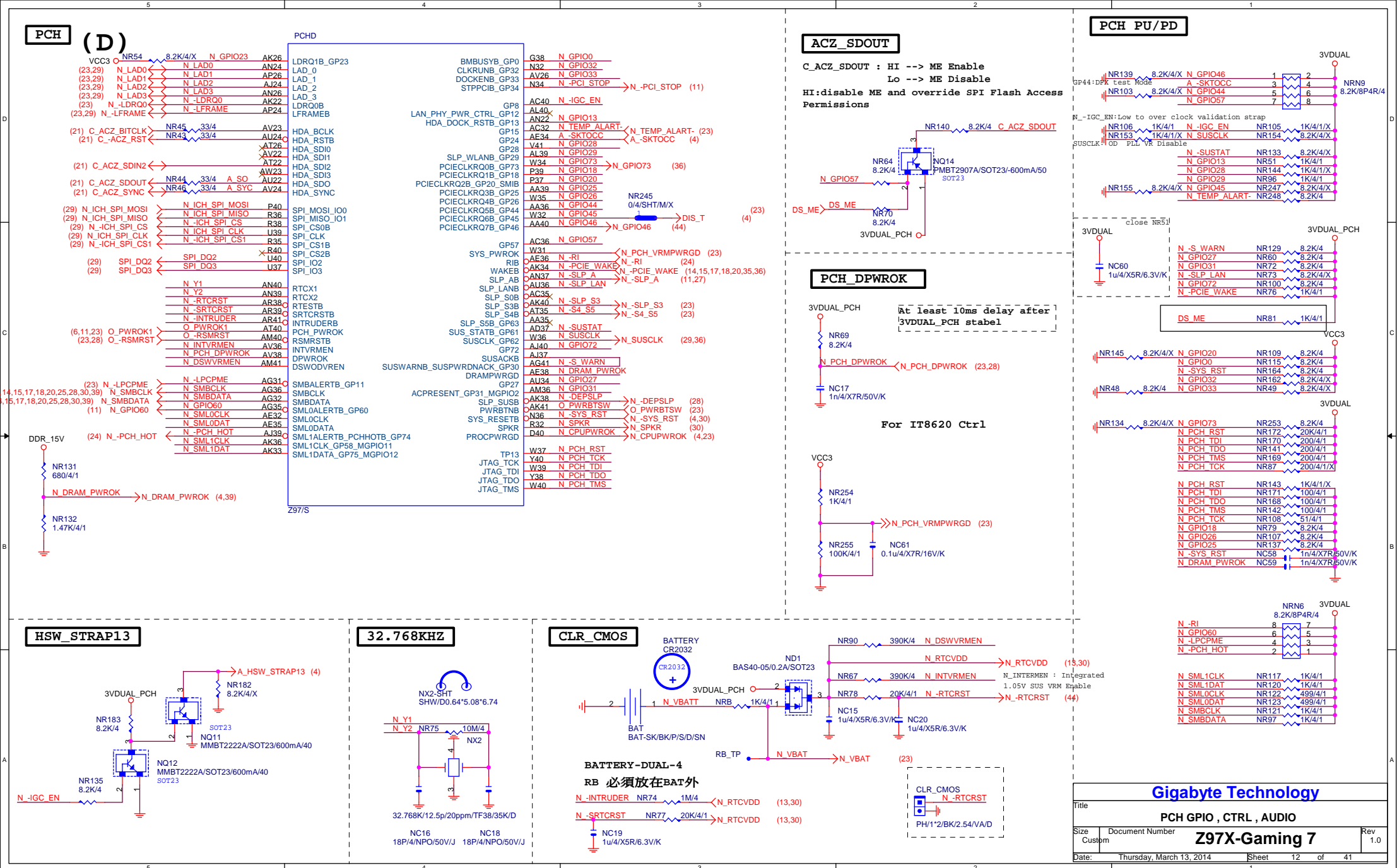
1

—

—

## Gigabyte Technology

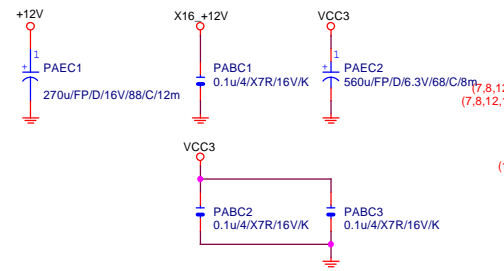
Title			
PCH HOST , SATA, PCI			
Size	Document Number		Rev
Custom	Z97X-Gaming 7		1.0
Date:	Thursday, March 13, 2014	Sheet	11 of 41





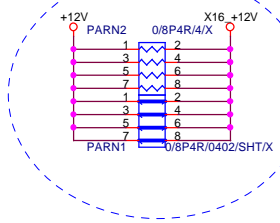


## PCIEX16 CAP



## PCIEX16 PROTECT SHT

+12 protect short-wire test



## PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u/4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u/4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u/4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u/4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u/4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u/4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u/4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u/4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u/4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u/4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u/4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u/4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u/4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u/4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u/4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u/4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP SW TXP8	PAC20	0.22u/4/X5R/6.3V/K	PA EXP SW TXP8 C
PA EXP SW TXN8	PAC21	0.22u/4/X5R/6.3V/K	PA EXP SW TXN8 C
PA EXP SW TXP9	PAC22	0.22u/4/X5R/6.3V/K	PA EXP SW TXP9 C
PA EXP SW TXN9	PAC23	0.22u/4/X5R/6.3V/K	PA EXP SW TXN9 C
PA EXP SW TXP10	PAC24	0.22u/4/X5R/6.3V/K	PA EXP SW TXP10 C
PA EXP SW TXN10	PAC25	0.22u/4/X5R/6.3V/K	PA EXP SW TXN10 C
PA EXP SW TXP11	PAC26	0.22u/4/X5R/6.3V/K	PA EXP SW TXP11 C
PA EXP SW TXN11	PAC27	0.22u/4/X5R/6.3V/K	PA EXP SW TXN11 C
PA EXP SW TXP12	PAC28	0.22u/4/X5R/6.3V/K	PA EXP SW TXP12 C
PA EXP SW TXN12	PAC29	0.22u/4/X5R/6.3V/K	PA EXP SW TXN12 C
PA EXP SW TXP13	PAC30	0.22u/4/X5R/6.3V/K	PA EXP SW TXP13 C
PA EXP SW TXN13	PAC31	0.22u/4/X5R/6.3V/K	PA EXP SW TXN13 C
PA EXP SW TXP14	PAC32	0.22u/4/X5R/6.3V/K	PA EXP SW TXP14 C
PA EXP SW TXN14	PAC33	0.22u/4/X5R/6.3V/K	PA EXP SW TXN14 C
PA EXP SW TXP15	PAC34	0.22u/4/X5R/6.3V/K	PA EXP SW TXP15 C
PA EXP SW TXN15	PAC35	0.22u/4/X5R/6.3V/K	PA EXP SW TXN15 C

PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWITH=2.5GHZ\*(8b/10b)=2Gb/s=250MB/s

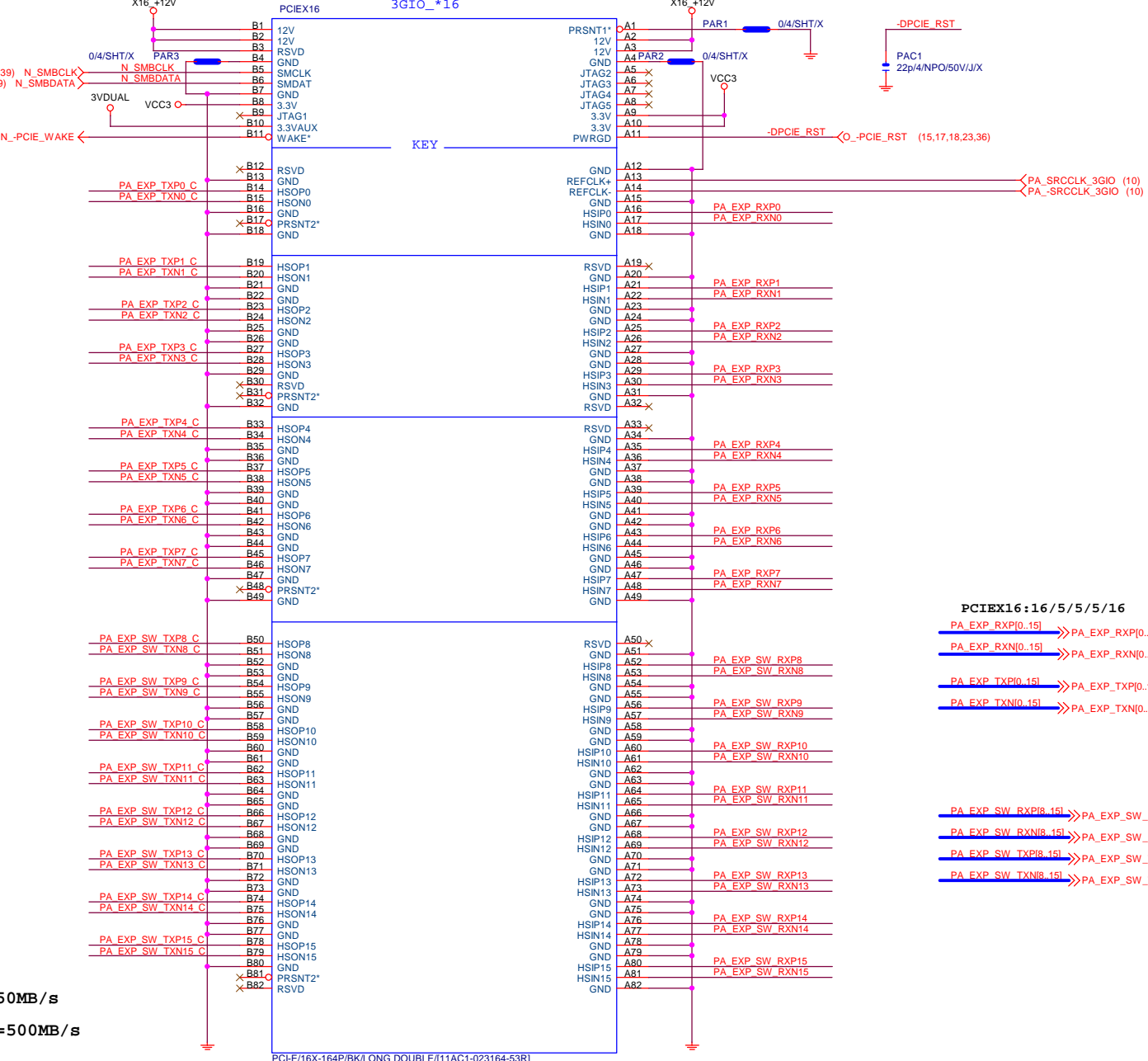
PCE-E X1(雙向) BANDWITH=2.5GHZ\*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWITH=2.5GHZ\*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWITH=2.5GHZ\*(8b/10b)X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--> 5GHZ

## PCIEX16 SLOT

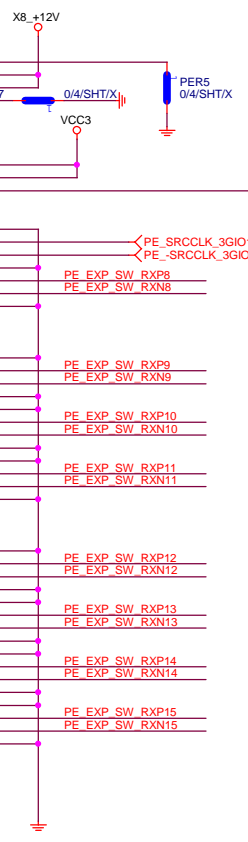
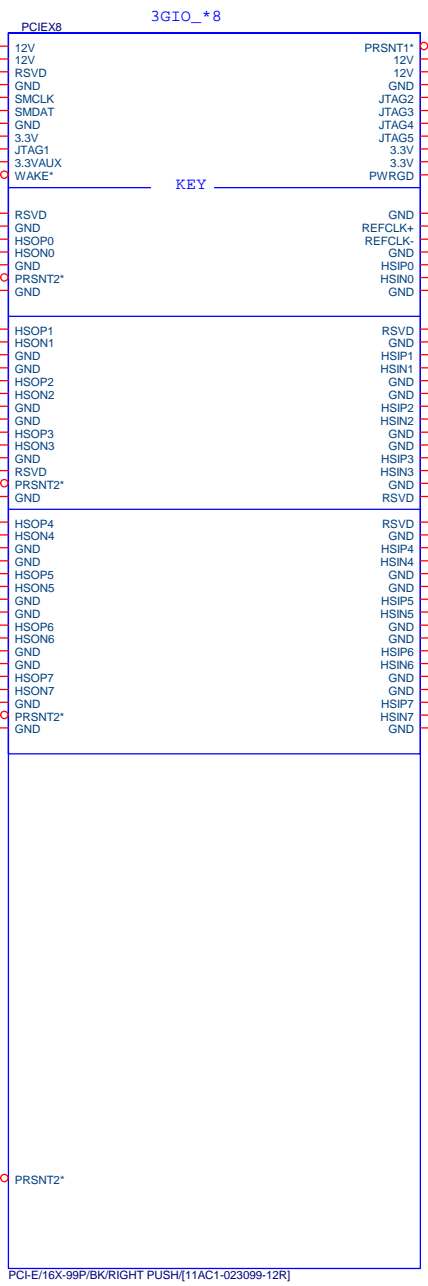
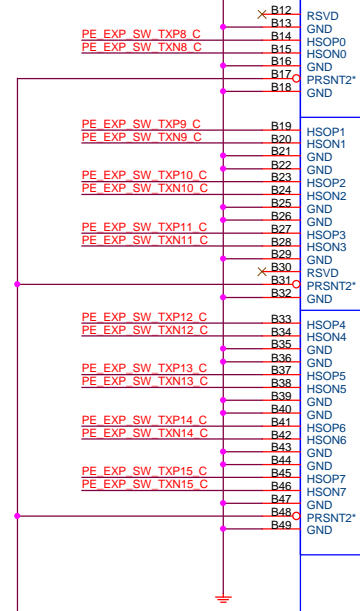
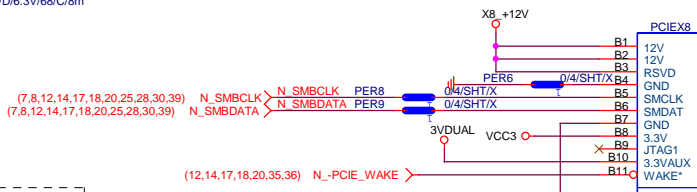
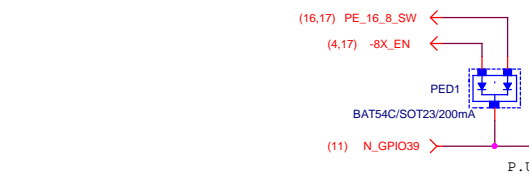
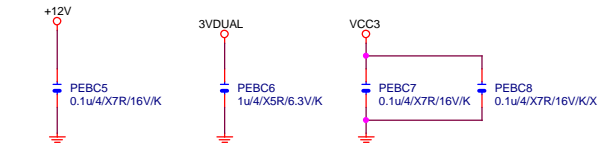
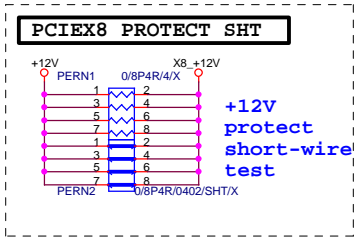
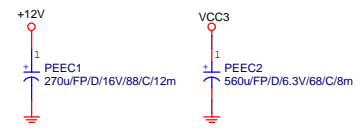


## PCIEX16:16/5/5/5/16

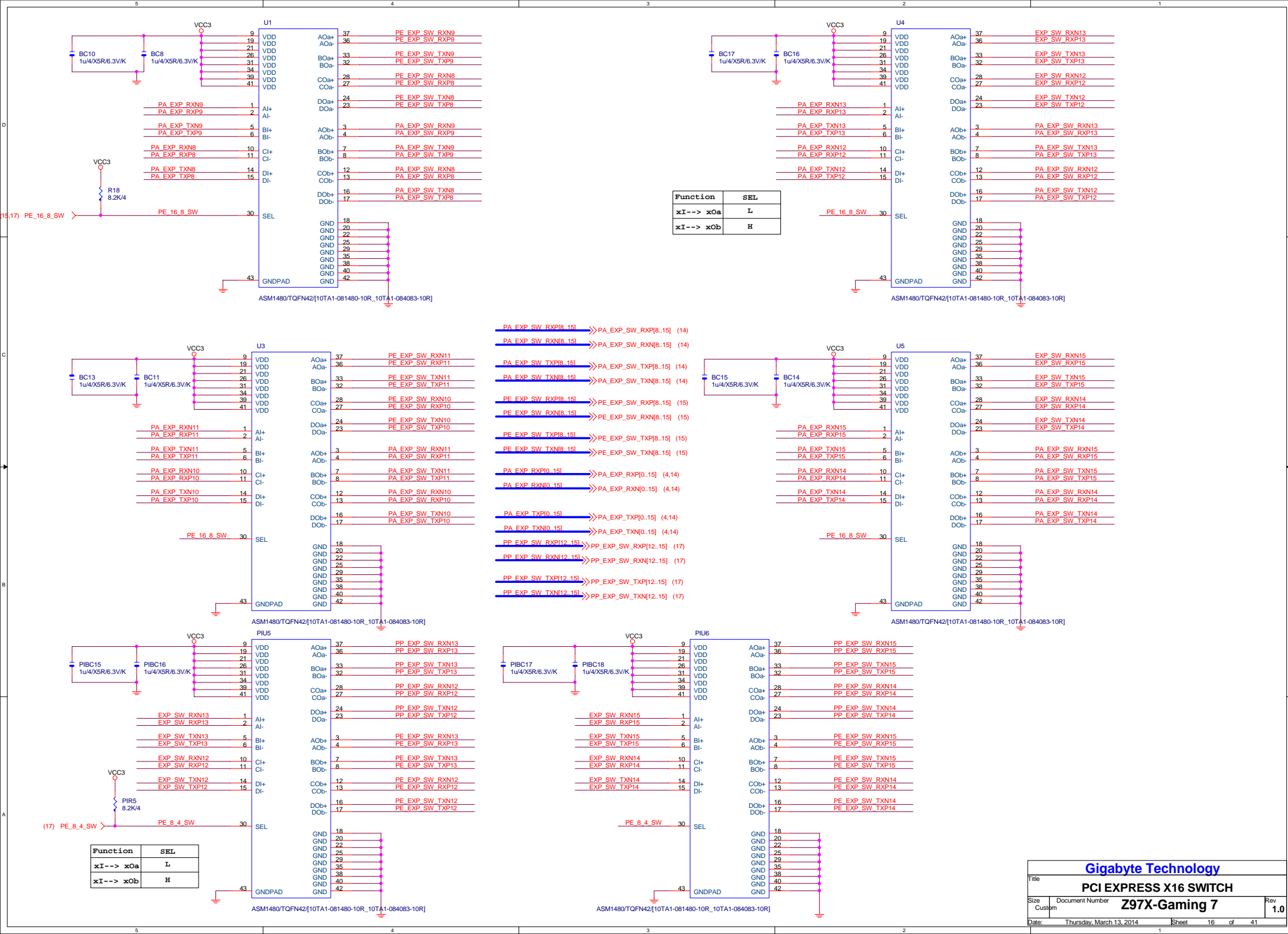
PA EXP_RXP[0..15]	>>>PA_EXP_RXP[0..15] (4,16)
PA EXP_RXN[0..15]	>>>PA_EXP_RXN[0..15] (4,16)
PA EXP_TXP[0..15]	>>>PA_EXP_TXP[0..15] (4,16)
PA EXP_TXN[0..15]	>>>PA_EXP_TXN[0..15] (4,16)
PA EXP_SW_RXP[8..15]	>>>PA_EXP_SW_RXP[8..15] (16)
PA EXP_SW_RXN[8..15]	>>>PA_EXP_SW_RXN[8..15] (16)
PA EXP_SW_TXP[8..15]	>>>PA_EXP_SW_TXP[8..15] (16)
PA EXP_SW_TXN[8..15]	>>>PA_EXP_SW_TXN[8..15] (16)

Gigabyte Technology			
PCI EXPRESS * 16			
Title	Document Number	Z97X-Gaming 7	
Size	Custom	Rev	1.0
Date:	Thursday, March 13, 2014	Sheet	14 of 41



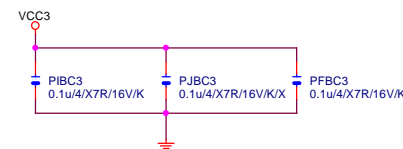
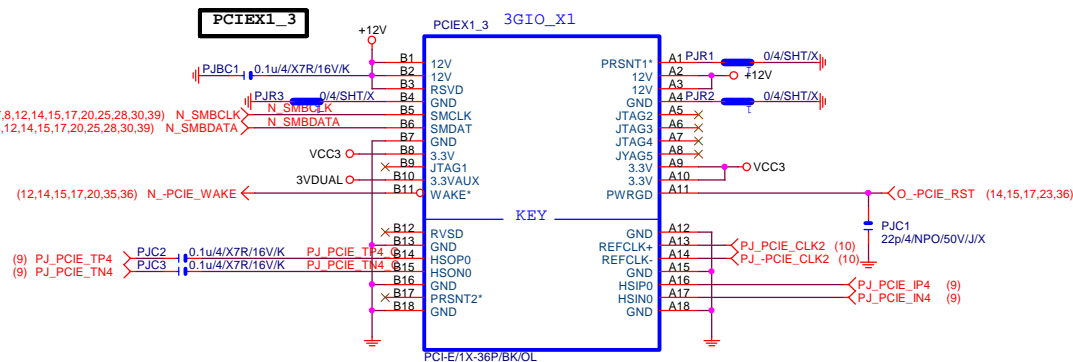
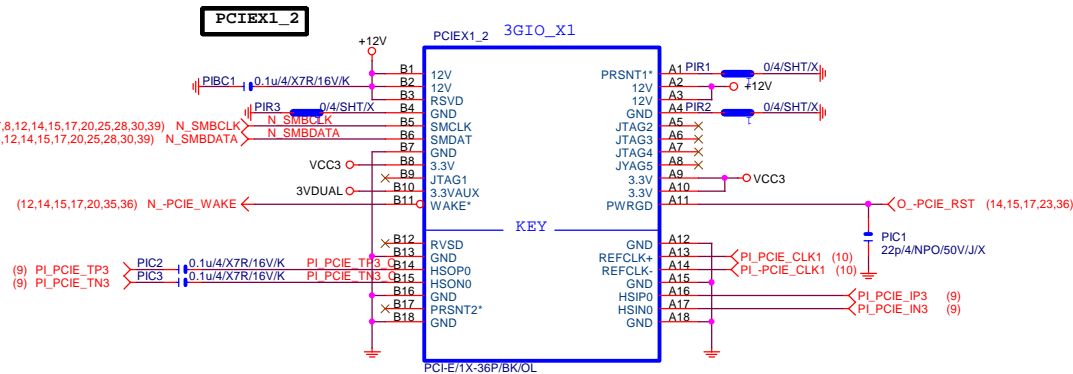
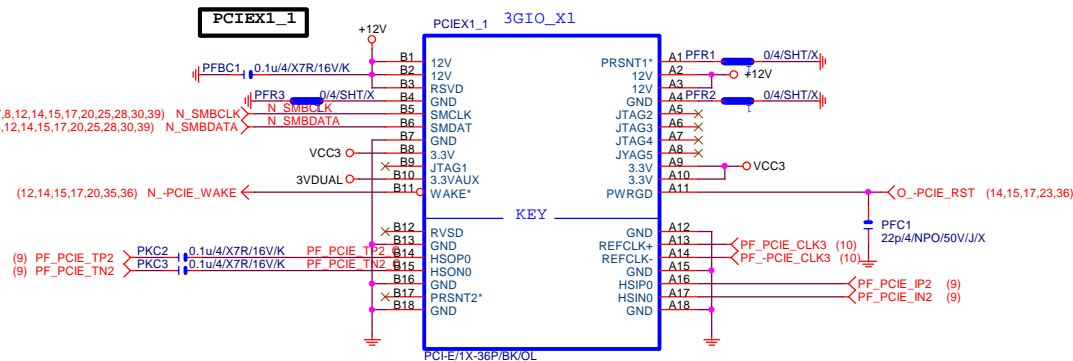


PE_EXP_SW_TXP8	PEC7	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXP8_C
PE_EXP_SW_TXN8	PEC8	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXN8_C
PE_EXP_SW_TXP9	PEC9	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXP9_C
PE_EXP_SW_TXN9	PEC10	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXN9_C
PE_EXP_SW_TXP10	PEC11	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXP10_C
PE_EXP_SW_TXN10	PEC12	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXN10_C
PE_EXP_SW_TXP11	PEC13	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXP11_C
PE_EXP_SW_TXN11	PEC14	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXN11_C
PE_EXP_SW_TXP12	PEC15	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXP12_C
PE_EXP_SW_TXN12	PEC16	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXN12_C
PE_EXP_SW_TXP13	PEC17	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXP13_C
PE_EXP_SW_TXN13	PEC18	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXN13_C
PE_EXP_SW_TXP14	PEC19	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXP14_C
PE_EXP_SW_TXN14	PEC20	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXN14_C
PE_EXP_SW_TXP15	PEC21	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXP15_C
PE_EXP_SW_TXN15	PEC22	0.22u4/X5R/6.3V/K	PE_EXP_SW_TXN15_C

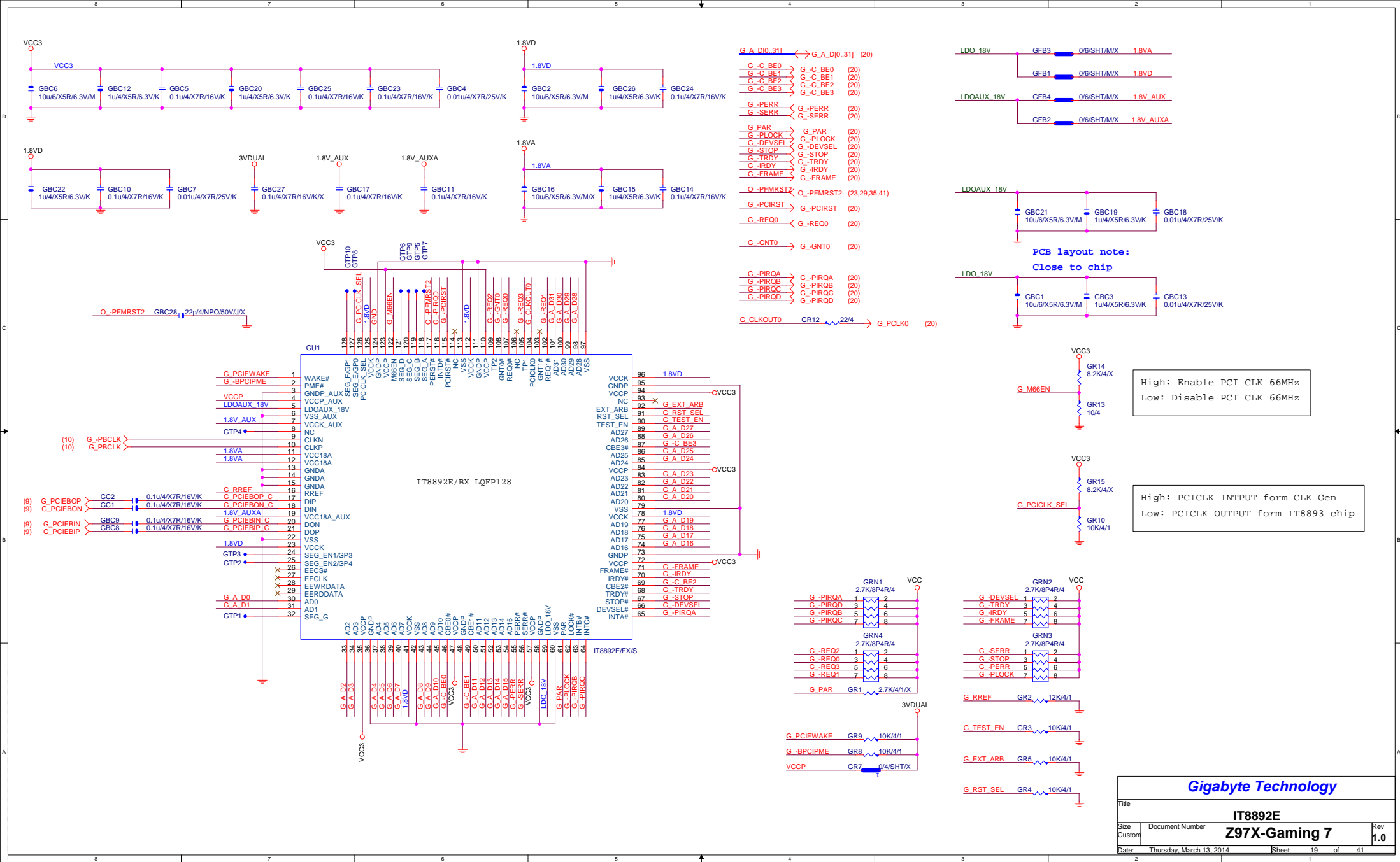




# PCIEX1 SLOT



Gigabyte Technology			
Title		PCIEX1_1,2,3	
Size	Document Number	Z97X-Gaming 7	
Custom			Rev 1.0
Date:	Thursday, March 13, 2014	Sheet 18	of 41







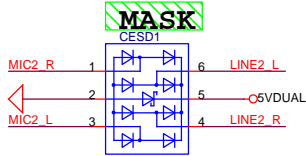
Thermal pad is DGND

Thermal pad is DGND

Digital Area

Analog Area

0/6/X For AGND/GND  
moat under Codec  
\_Body



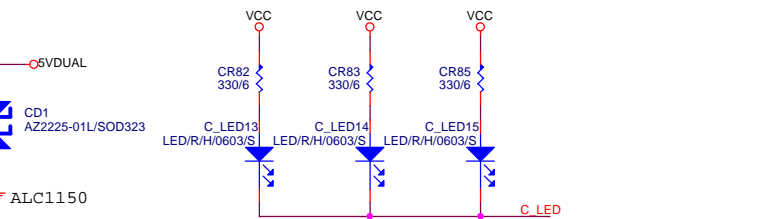
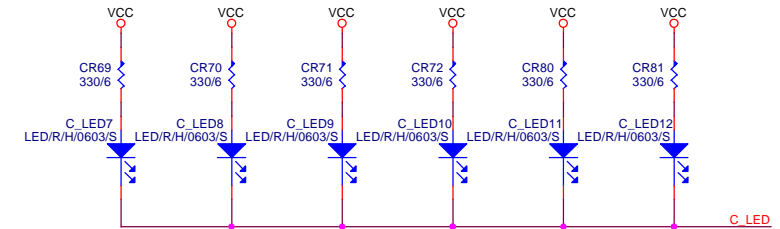
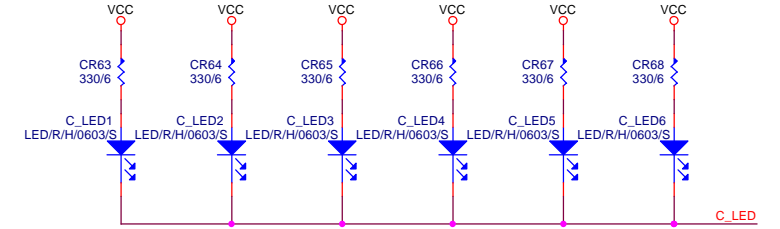
AZC099-04S.R7G/SOT23-6L[10DEF-550099-20R\_10TA1-018902-10R]X

EAPD: Default L  
H : ON  
L : OFF

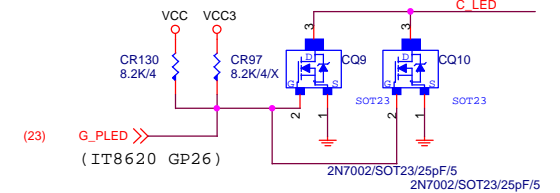
Close to ALC1150

金屬外罩+  
GND切割

AUDIO\_HS[11NH1-00Z97S-01R]

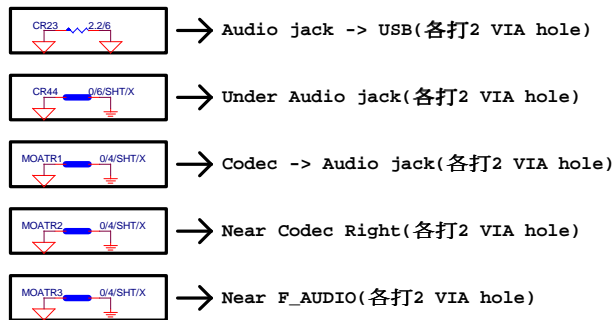


MOAT LED

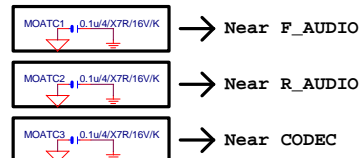
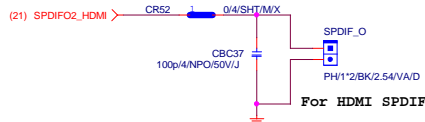


Gigabyte Technology

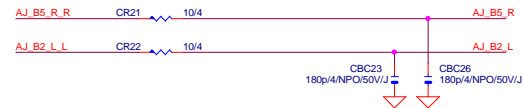
Title			HD AUDIO ALC887B-VD2/VT1708SVT2021	
Size	Custom	Document Number	Z97X-Gaming 7	
Date:	Thursday, March 13, 2014	Sheet	21	of 41
			Rev	1.0



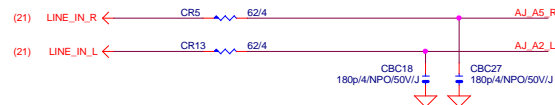
## SPDIF\_OUT



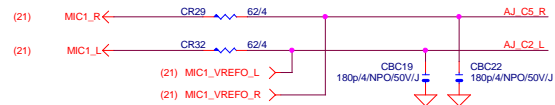
## LINE-OUT



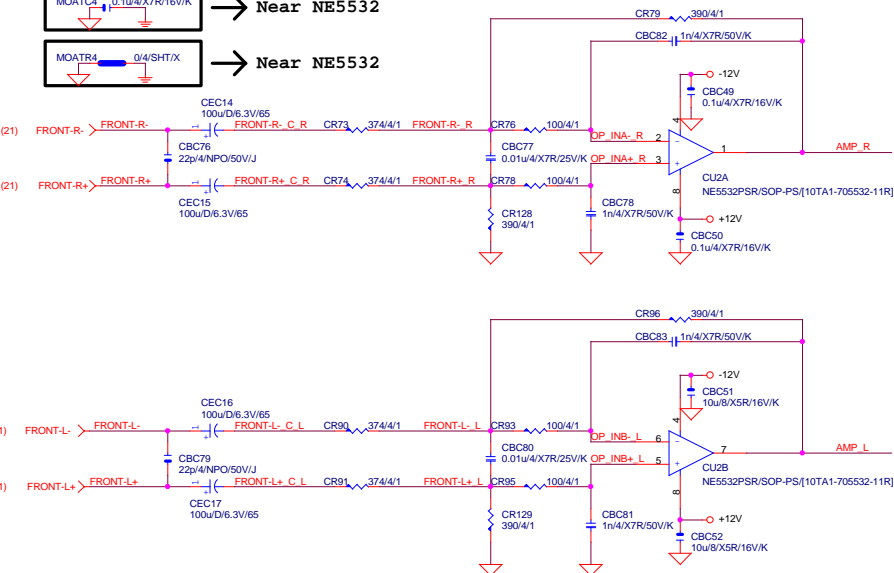
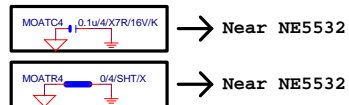
**LINE-IN**



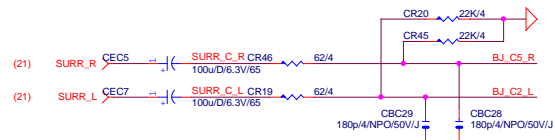
## MIC-IN



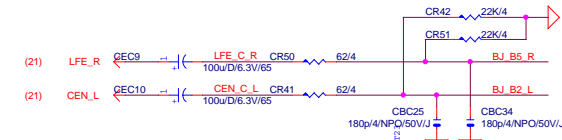
## Differential to Single-End AMPLIFIED



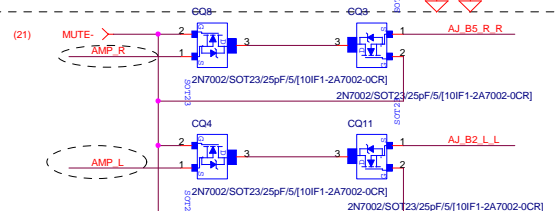
**SURROUND**



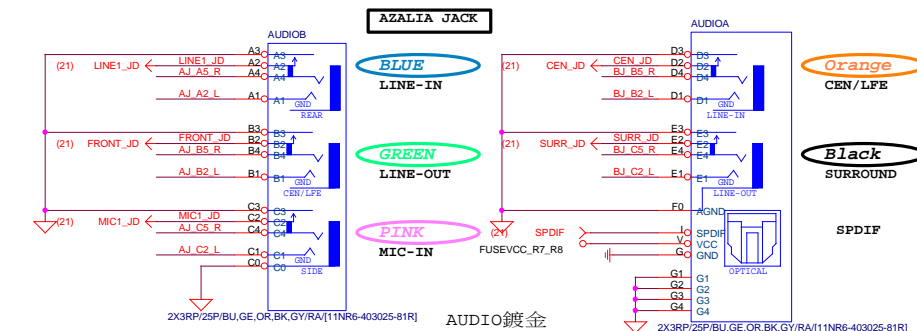
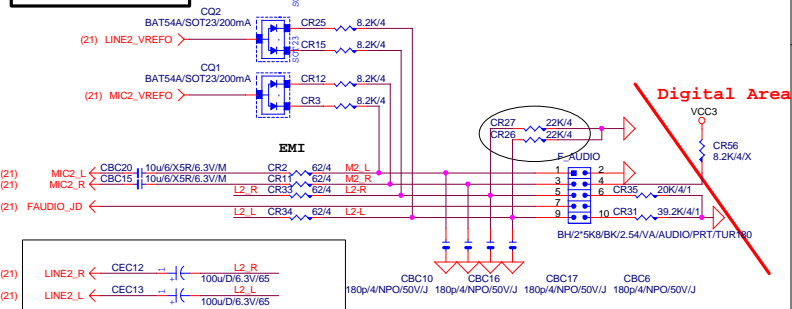
## CEN/LFE




**SURR BACK**



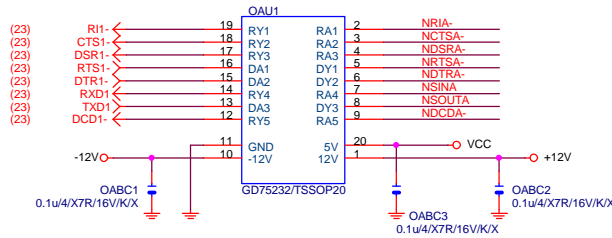
## AZALIA FRONT PANEL



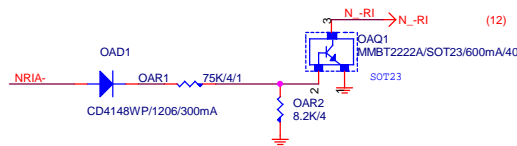
<div style="text-align: center;">  </div>			
Title			
<b>AUDIO JACK</b> <b>Z97X-Gaming 7</b>			
Size Custom	Document Number		Rev 1.0
Date:	Thursday, March 13, 2014	Sheet	22 of 41



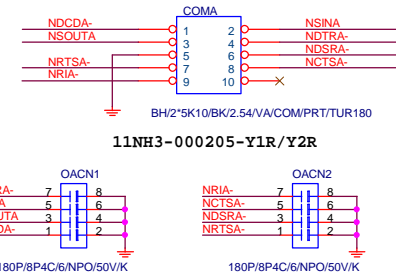
## COMA



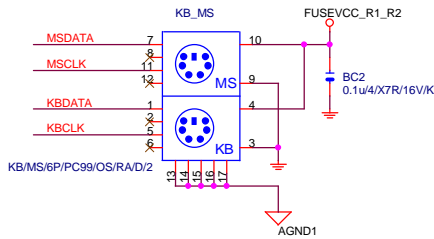
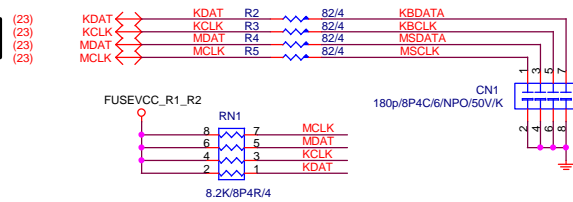
## COM RI



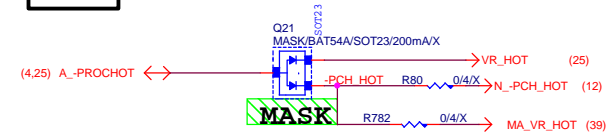
## COM BUFFER



## KB/USB



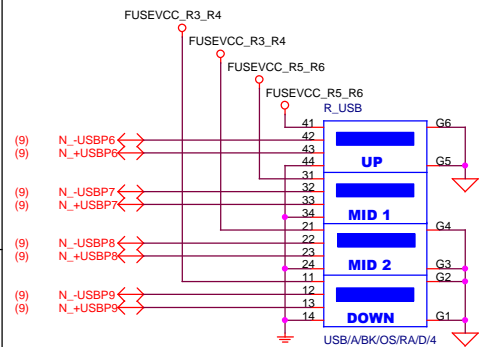
## -PROHOT



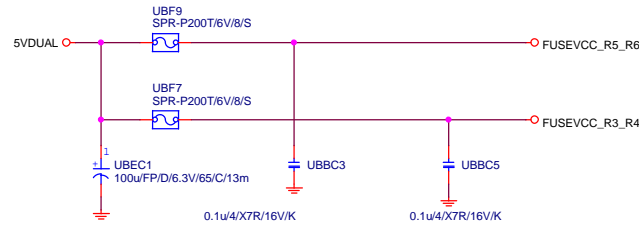
## Thunderbolt pin header

Removed

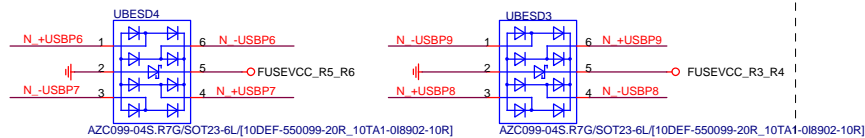
## R\_USB



## USB20 FUSE

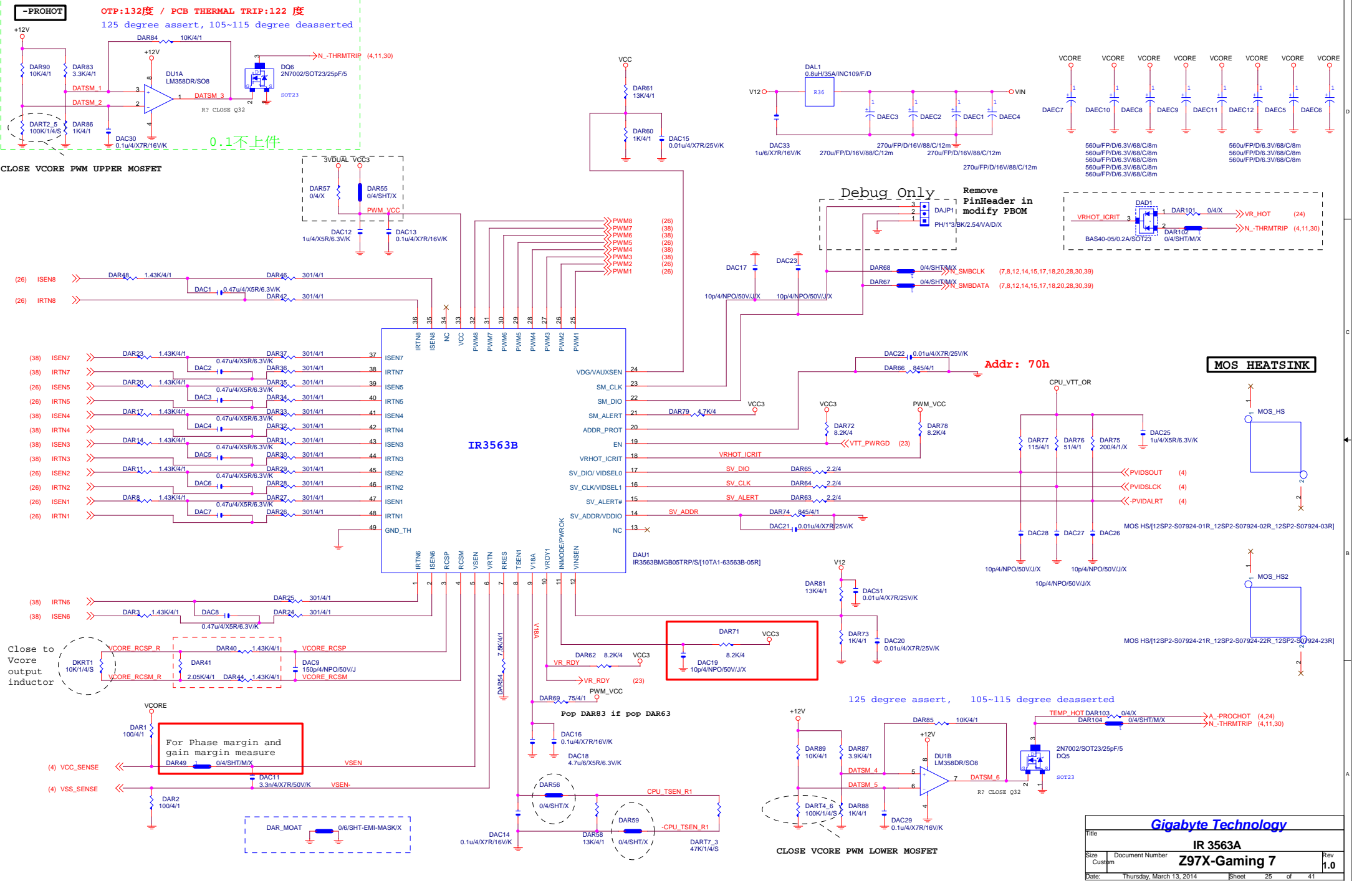


## USB20 ESD PROTECT



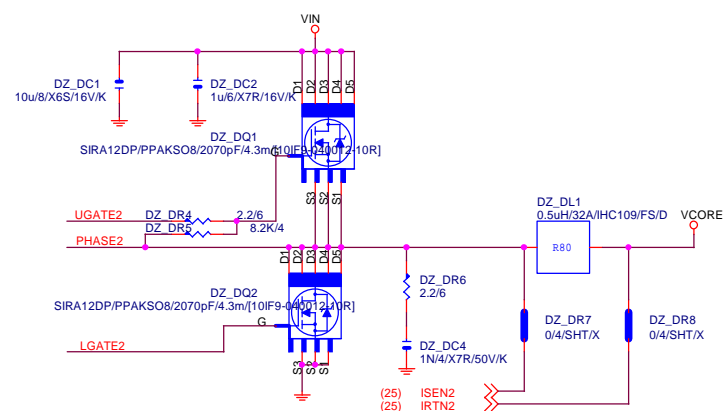
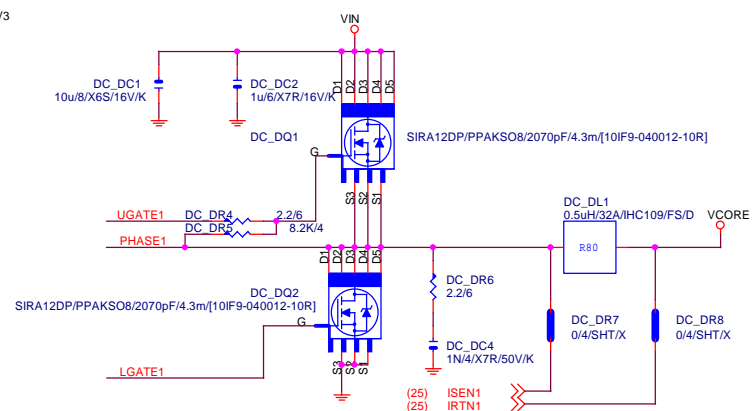
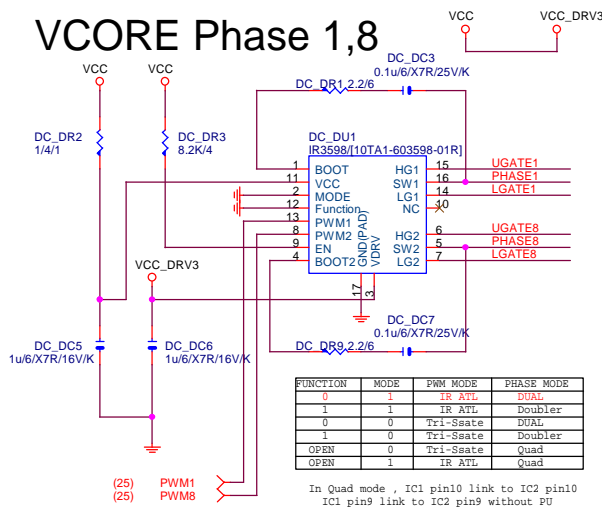
Gigabyte Technology

Title			
COM/ PROHOT/ R_USB			
Size	Document Number	Rev	
Custom	Z97X-Gaming 7	1.0	
Date:	Thursday, March 13, 2014	Sheet	24 of 41

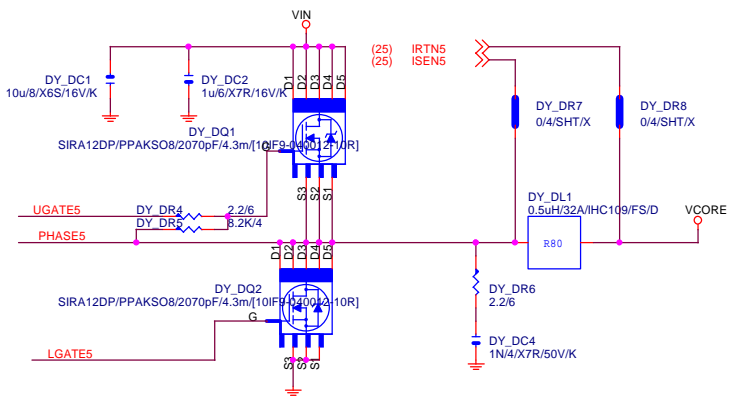
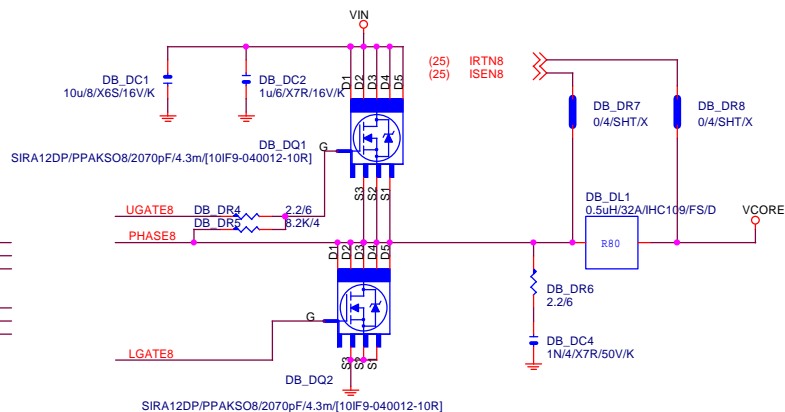
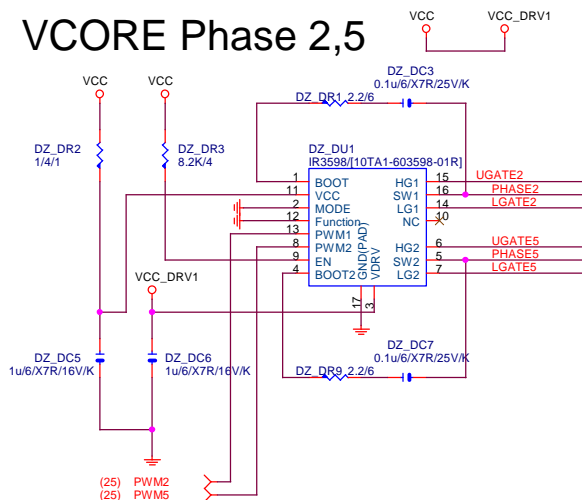


Gigabyte Technology			
IR 3563A			
Z97X-Gaming 7			
Rev	1.0		
Date:	Thursday, March 13, 2014	Sheet	25 of 41

## VCORE Phase 1,8

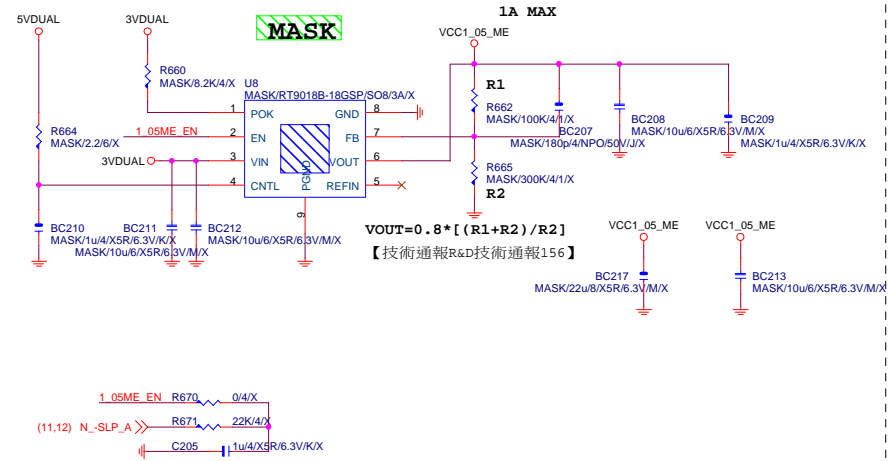


## VCORE Phase 2,5

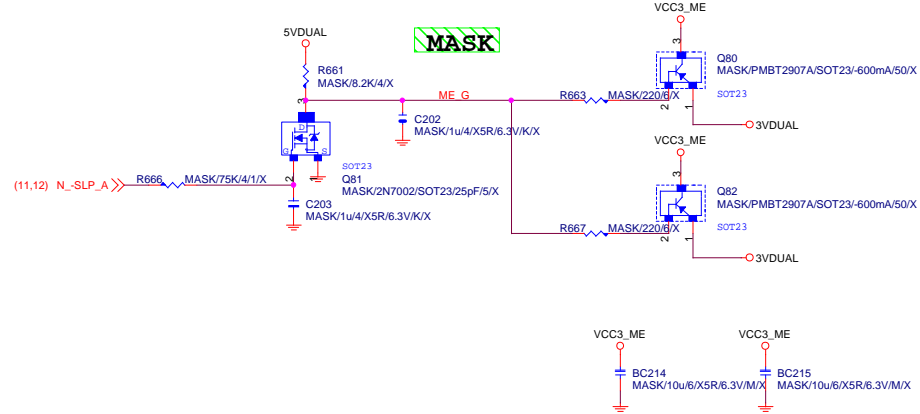




VCC1\_05\_ME

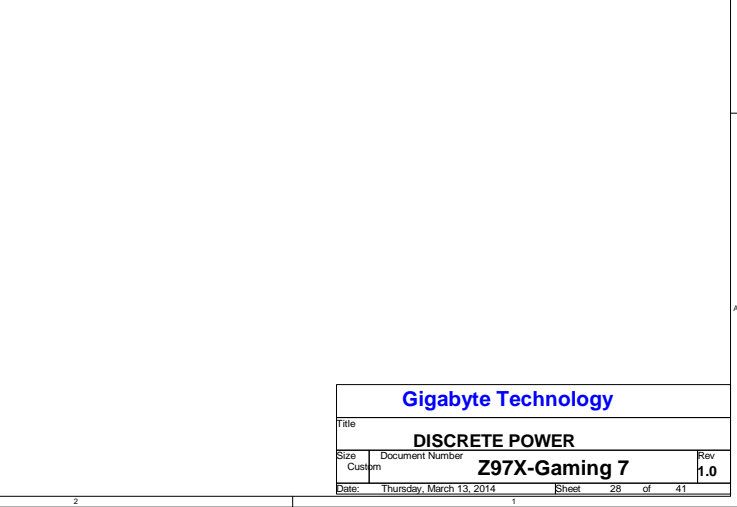
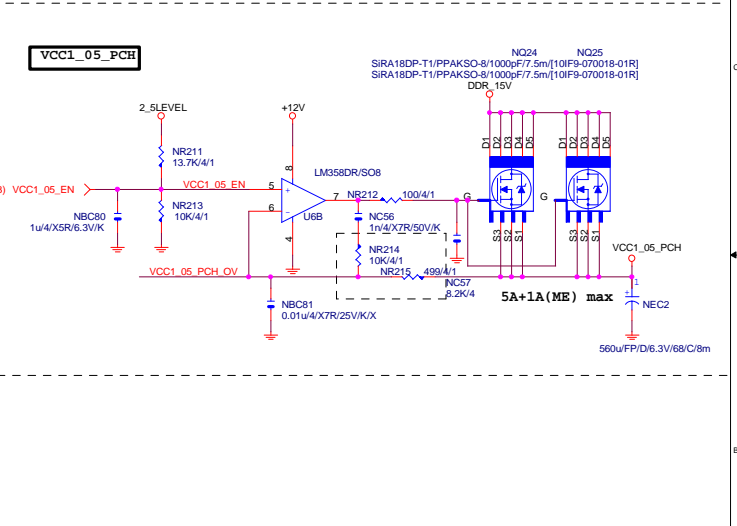
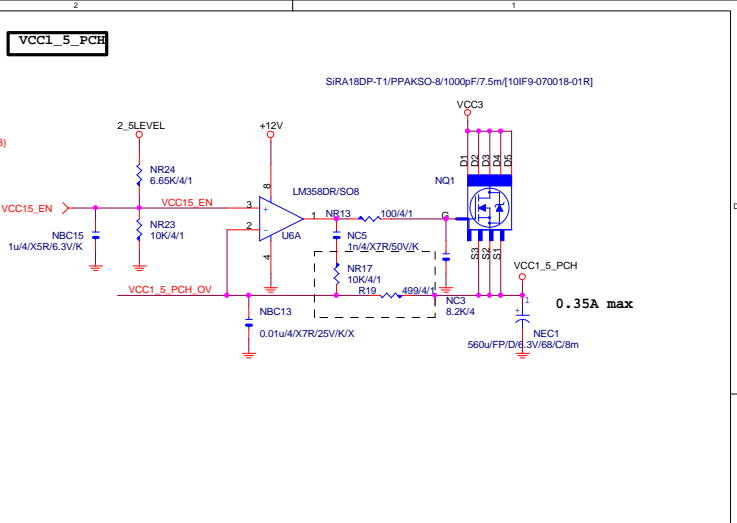


VCC3\_ME



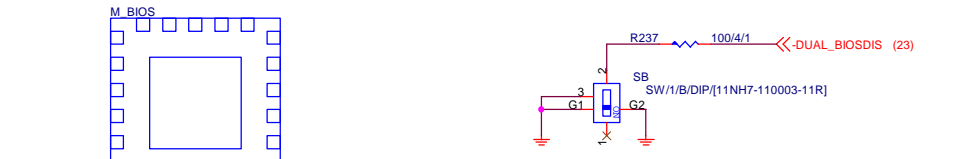
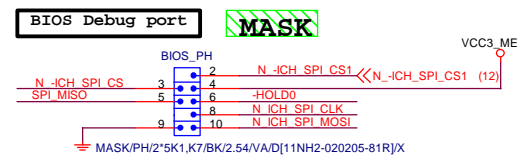
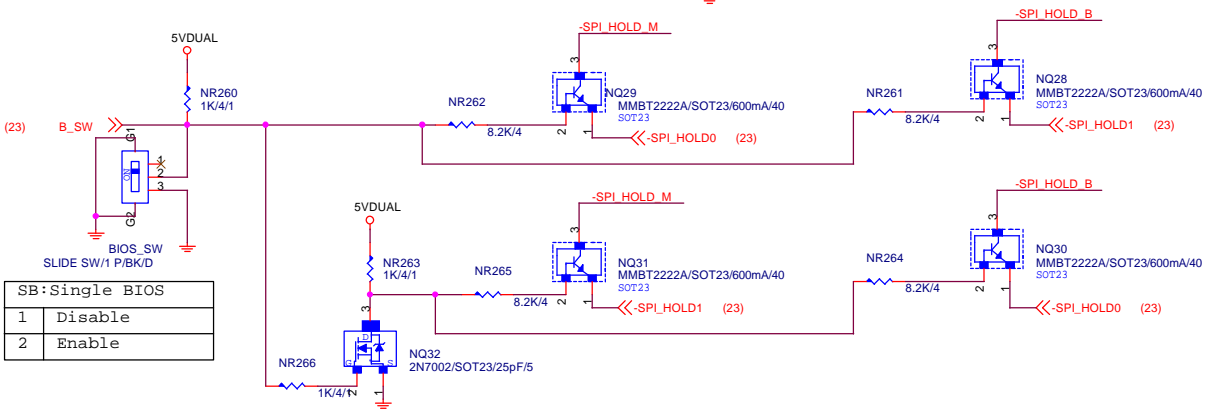
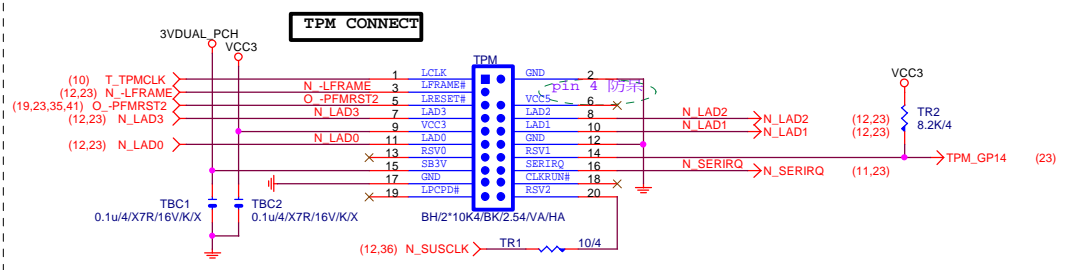
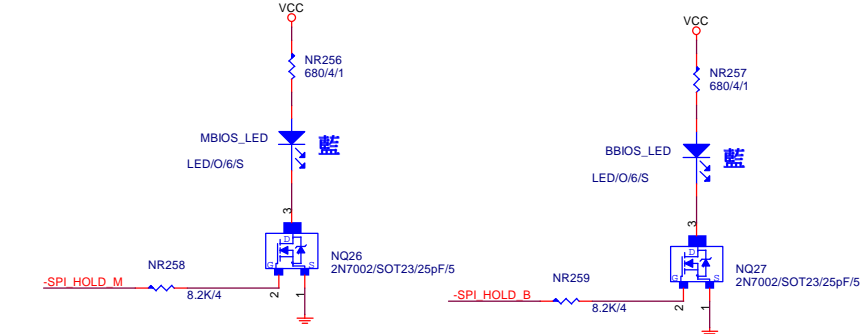
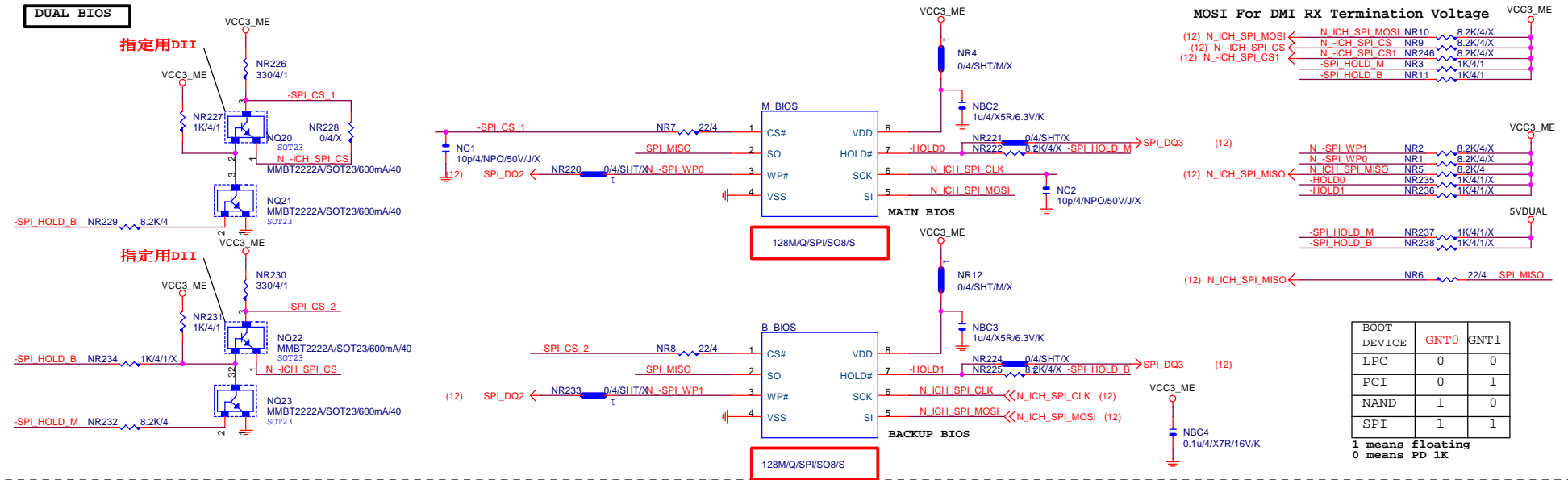
DDR 15V

DDRVTT



---

DUAL BIOS



SB:Single BIOS	
1	Disable
2	Enable

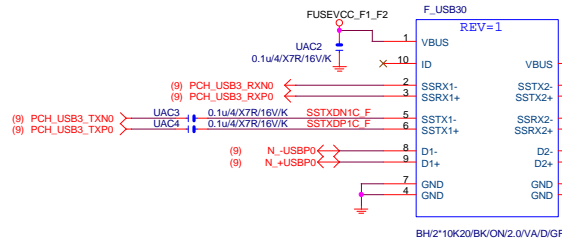
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1	means floating
0	means PD 1K

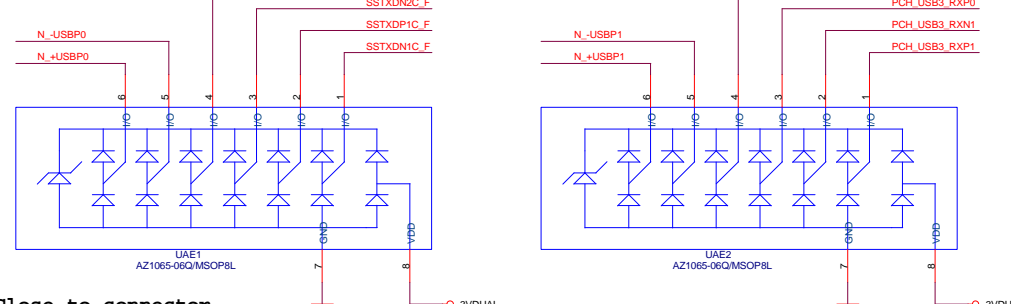
**Gigabyte Technology**

Title		<b>DUAL BIOS, TPM</b>	
Size	Document Number	<b>GA-Z97X-UD5H</b>	Rev
Custom			<b>1.0</b>
Date:	Thursday, March 13, 2014	Sheet	29 of 45

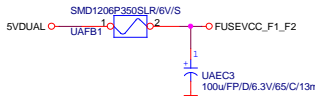
Front USB3.0



F\_USB30 ESD PROTECT

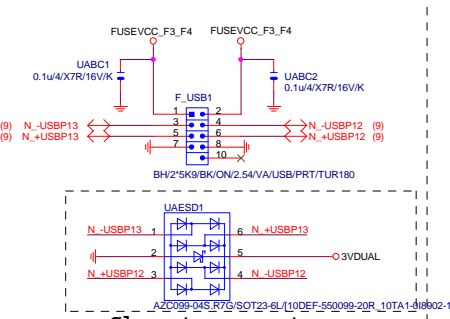


F\_USB30 PWR



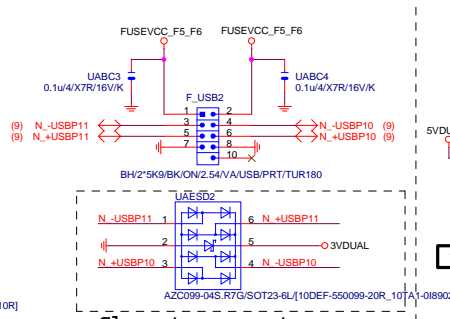
Close to connector

FRONT USB1



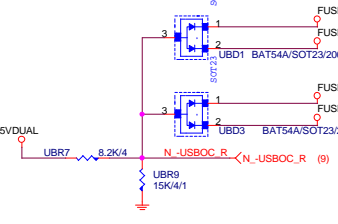
Close to connector

FRONT USB2

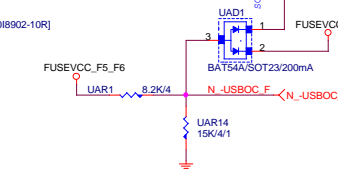


Close to connector

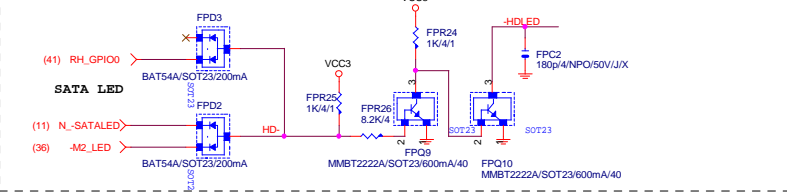
-USBOC\_R



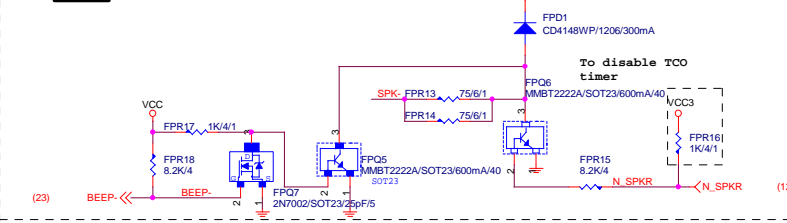
-USBOC\_F



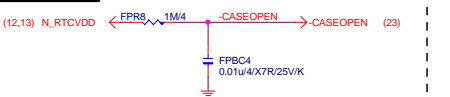
SATA LED



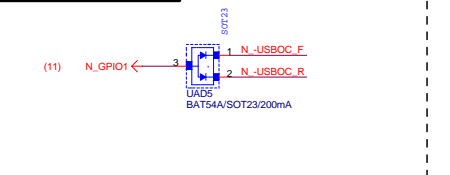
SPKR



CASE OPEN



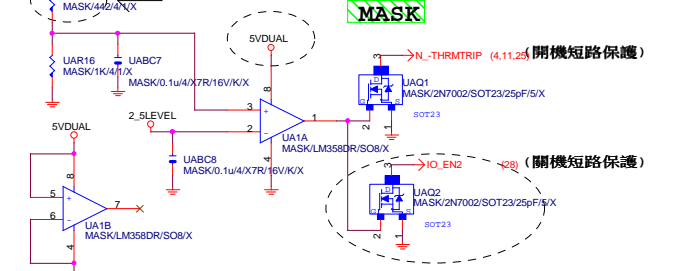
F\_USB POWER PROTECT



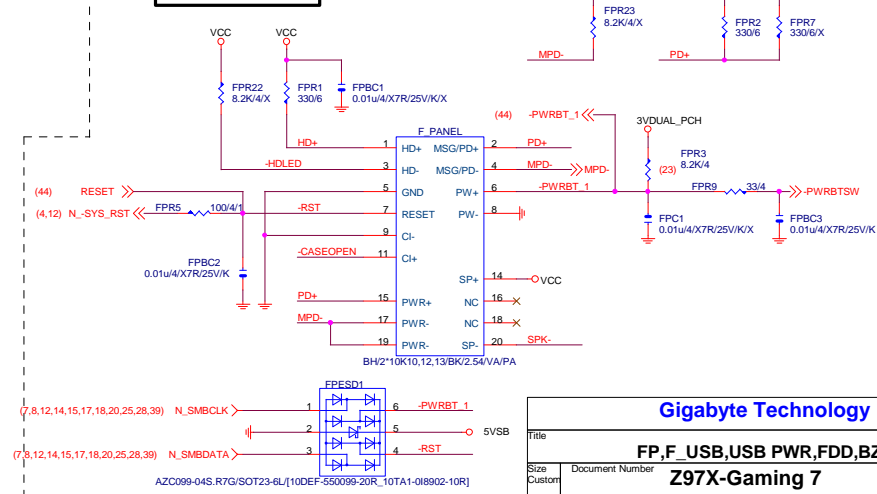
USB2.0 Signal & power short protection

USB2.0 Signal > 4.85V

Enable --> 3VDDUAL=3.5V



INTEL FRONT PANEL



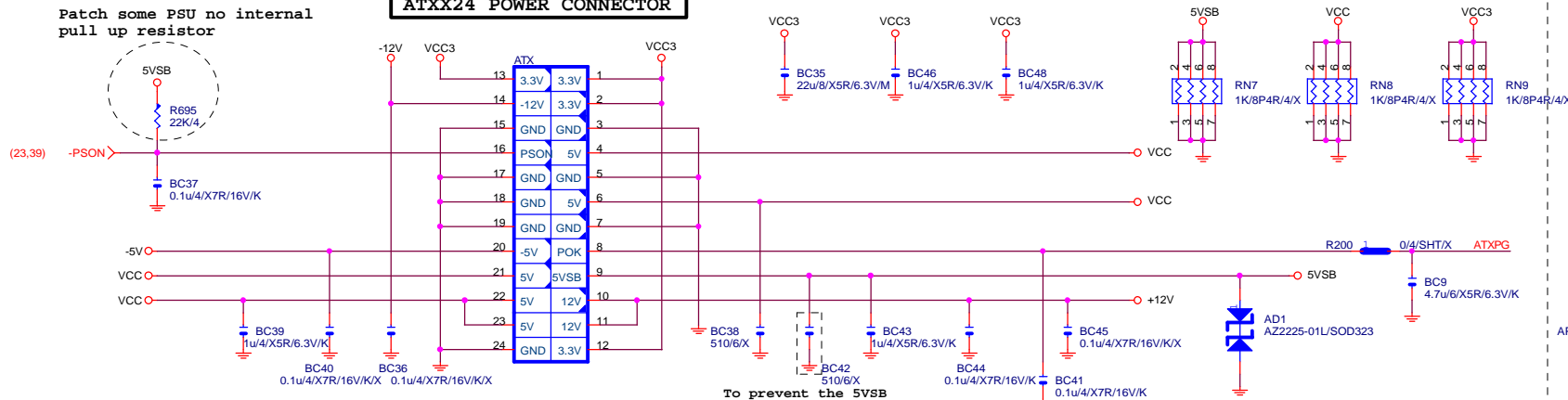
Gigabyte Technology

FP,F\_USB,USB PWR,FDD,BZ  
Z97X-Gaming 7

Title	Document Number	Rev
Size	Custom	1.0
Date	Thursday, March 13, 2014	Sheet 30 of 41

Patch some PSU no internal pull up resistor

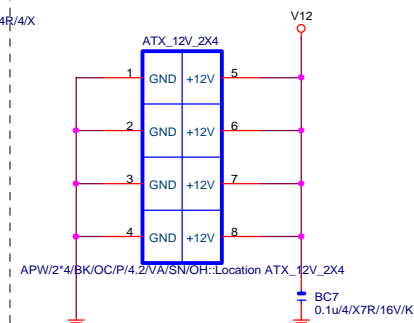
## ATXX24 POWER CONNECTOR



APW/2\*12/BK/VA/SN/2SHK/PA66

To prevent the 5VSB under loading when boot

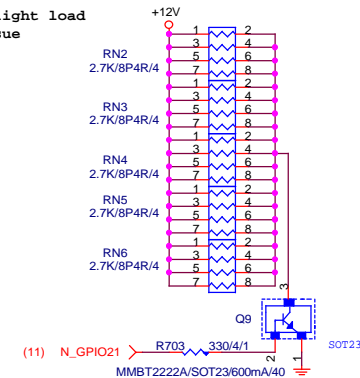
## ATXX4 POWER CONNECTOR



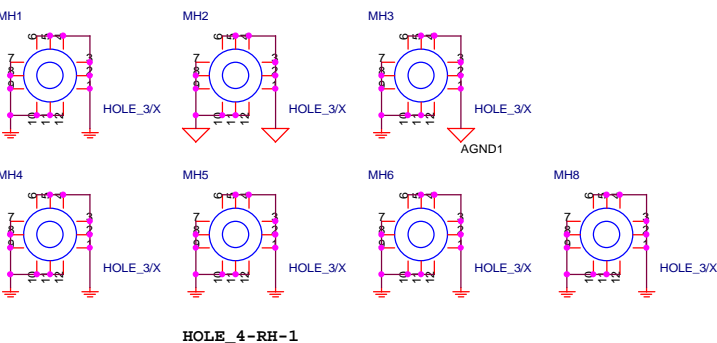
APW/2\*4/BK/OC/P/4.2/VA/SN/OH/Location ATX\_12V\_2X4

## 【技術通報R&D技術通報153】

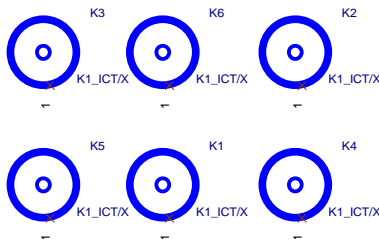
To fix 12V light load abnormal issue



(11) N\_GPIO21 R703 330/4/1 MMBT2222A/SOT23/600mA/40



HOLE\_4-RH-1



K1-ICT



Date: Thursday, March 13, 2014 Sheet 32 of 41

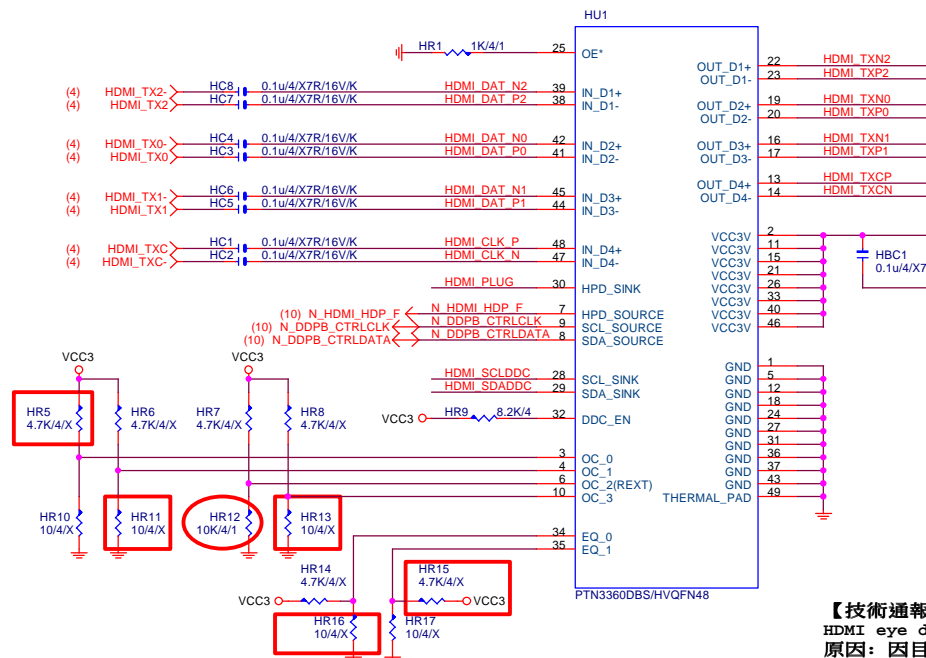




# HDMI LEVEL SHIFT

HDMI:20/4/6/4/20

Impedance=85 +- 17.5%



PTN3360:PIN 4/10/34/35 NC PIN,都不上值;只上HR12:10K  
ASM1442:紅色框要上,HR12:3.16K

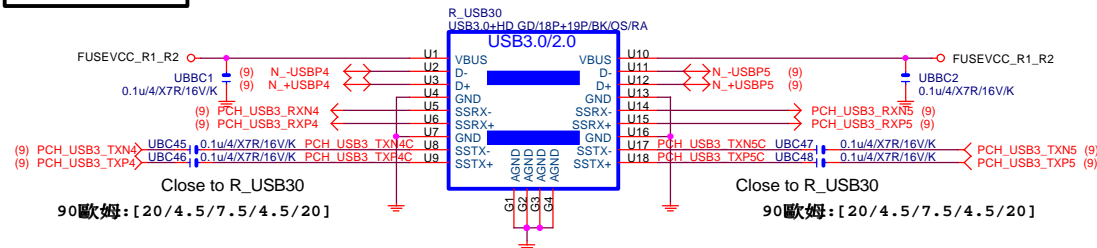
## 【技術通報R&D技術通報150】

HDMI eye diagram1.4版(deep color)會fail

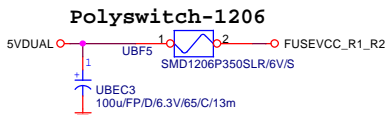
原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram

改善: ASMEDIA ASM1442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)

# USB30\_20 CONNECT

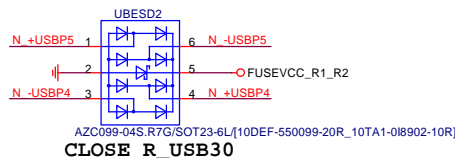


# USB30 PWR

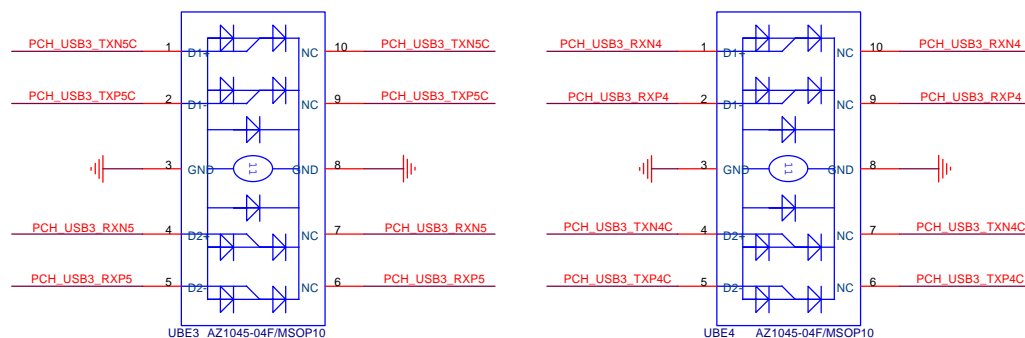


USB3.0 1Port - 1Fuse (3.5A)

# USB20 ESD PROTECT



# USB30 ESD PROTECT



# Gigabyte Technology

Title		
HDMI		
Size	Document Number	Rev
Custpm	Z97X-Gaming 7	1.0
Date:	Thursday, March 13, 2014	Sheet 34 of 41



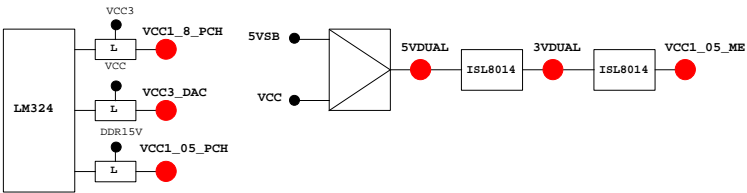


PCH GPIO LIST TABLE					
PIN NAME	PWR	Default	USAGE	NOTE	
GP0	MAIN	H-Z	GPI	GPIO0	N/A
GP1/TACH1	MAIN		GPI	GPIO1	N/A
GP2/PIRQE#	MAIN		GPI	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN		GPI	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN		GPI	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN		GPI	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN		GPI	PCIEX1 Detect	P/U 8.2K VCC3
GP7/TACH3	MAIN		GPI	GPIO7	P/U 8.2K VCC3
GP8	STBY	H	GPI	GPIO8	N/A
GP9/OC5#	STBY		NATIVE	USB OC5#	N/A
GP10/OC6#	STBY		NATIVE	USB OC6#	N/A
GP11/SMBALERT#	STBY		NATIVE	USB PWR protect	P/U 8.2K 3VDUAL
GP12	STBY	L	GPI	GPIO12	N/A
GP13	STBY	L	GPI	LPCPME#	P/U 8.2K 3VDUAL
GP14/OC7#	STBY		NATIVE	USB OC7#	N/A
GP15	STBY	L	GPI	GPIO15(TLS Enable)	P/U 8.2K 3VDUAL
GP16	MAIN		GPI	GPIO16	P/U 8.2K VCC3
GP17/TACH0	MAIN		GPI	GPIO17	P/U 8.2K VCC3
GP18	MAIN		GPI	Mobile Only	N/A
GP19	MAIN		GPI	GPIO19	P/U 8.2K VCC3
GP20	MAIN		GPI	GPIO20	P/U 8.2K VCC3
GP21	MAIN		GPI	GPIO21	P/U 8.2K VCC3
GP22	MAIN	H-Z	GPI	GPIO22	P/U 8.2K VCC3
GP23	MAIN		GPI	GPIO23	N/A
GP24	STBY	L	GPI	SKTOCC#	N/A
GP25	STBY			Mobile Only	N/A
GP26	STBY			Mobile Only	N/A
GP27	STBY	H	GPO	GPIO27	P/U 8.2K 3VDUAL
GP28	STBY	H	GPO	PWR LED	P/U 8.2K 3VDUAL
GP29	STBY	L	GPI	GPIO29	N/A
GP30	STBY	H-Z	GPI	Mobile Only	N/A
GP31	STBY	H-Z	GPI	Mobile Only	N/A
GP32	MAIN	H	GPO	N/A	N/A
GP33	MAIN	H	GPO	N/A	N/A
GP34	MAIN	H-Z	GPI	-PCI_STOP	P/U 8.2K VCC3
GP35	MAIN	L	GPO	-ACZ_DET	P/U 8.2K VCC3
GP36	MAIN		GPI	N/A	N/A
GP37	MAIN		GPI	N/A	N/A
GP38	MAIN	H-Z	GPI	PCIEX4 Detect	P/U 8.2K VCC3
GP39	MAIN	H-Z	GPI	GPIO39	P/U 8.2K VCC3
GP40	STBY		NATIVE	USB OC1#	N/A
GP41	STBY		NATIVE	USB OC2#	N/A
GP42	STBY		NATIVE	USB OC3#	N/A
GP43	STBY		NATIVE	USB OC4#	N/A
GP44	STBY	L	NATIVE	GPIO44	P/U 8.2K 3VDUAL
GP45	STBY		NATIVE	GPIO45	P/U 8.2K 3VDUAL
GP46	STBY	L	NATIVE	GPIO46	P/U 8.2K 3VDUAL
GP47	STBY			Mobile Only	N/A
GP48	MAIN	H-Z	IN	GPIO48	P/U 8.2K 3VDUAL
GP49	MAIN	H-Z	IN	GPIO49	P/U 8.2K 3VDUAL
GP50	MAIN		NATIVE	-REQ1	P/U 2.2K VCC
GP51	MAIN	H	NATIVE	-GNT1	N/A
GP52	MAIN		NATIVE	-REQ2	P/U 2.2K VCC
GP53	MAIN	H	NATIVE	-GNT2	N/A
GP54	MAIN		NATIVE	-REQ3	P/U 2.2K VCC
GP55	MAIN	H	NATIVE	-GNT3	N/A
GP56	STBY		NATIVE	Mobile Only	N/A
GP57	STBY	H-Z	IN	VCORE_OV1	P/U 8.2K 3VDUAL
GP58	STBY	H-Z	NATIVE	F_USB_OC	P/U 8.2K 3VDUAL
GP59	STBY		NATIVE	USB_OC0#	N/A
GP60	STBY	H-Z	NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL
GP61	STBY	L	NATIVE	-SUSTAT	N/A
GP62	STBY	L	NATIVE	SUSCLK	N/A
GP63	STBY	L	NATIVE	GPIO63	N/A
GP64	MAIN	L	NATIVE	CLKOUTFLEX0	N/A
GP65	MAIN	L	NATIVE	CLKOUTFLEX1	N/A
GP66	MAIN	L	NATIVE	CLKOUTFLEX2	N/A
GP67	MAIN	L	NATIVE	CLKOUTFLEX3	N/A
GP72	STBY	H-Z	NATIVE	VCORE_OV4	P/U 8.2K 3VDUAL
GP73	STBY			Mobile Only	N/A
GP74	STBY	H-Z	NATIVE	1_05V_OV2	P/U 8.2K 3VDUAL
GP75	STBY	H-Z	NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL

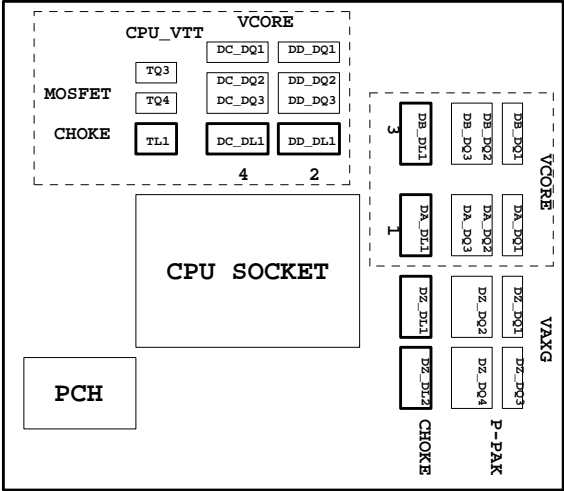
Super I/O ITE8720 GPIO Table

PIN NAME	USAGE	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KBRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCI_E_RST	
RSMRST#CIRRXL/GP55	-RSMRST	
PME#/GP54	-LPCPME	
PD5/GP75/BUSS00	N/A	

PIN NAME	USAGE	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSSI1	SB_LED1_C	
PD4/GP74/BUSSI2	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSSIO	NB_LED3_C	
GP22/SCK	LOW_PWR_1	
VIDO5/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PFMRST1	
PCIRST1#/GP12	-PFMRST2	
3VBSBW#/GP40	CSI_F0	BSEL166_1
SUSC#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VIDO0/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSS01	MB_ID3	
PD7/GP77/BUSS02	MB_ID4	
AFD#/GP86/SMBC_R	SEC_PIN	FST_2X8
INIT#/GP85/SMBD_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VIDO1/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMBC_M	DDR_LED3_C	
PWRON#/GP44	VCORE_OV1	
PANSWH#/GP43	PWRBTSW	
KDAT/GP61	-PWRBTSW	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRTX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#/GP84/SMBD_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRXL2/GP16	-THERM	
VIDO4/GP26/SOUT2	DDR18V_PH2_EN	
VIDO2/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VIDO6/GP17/RI2#	1_1V_PH_EN	
VIDO7/JP6/DTR2#	JP6	
PD5/GP75/BUSS00	SB_LED3_C	



PWM各相位的擺法如下：



BIOS超電壓對應表：

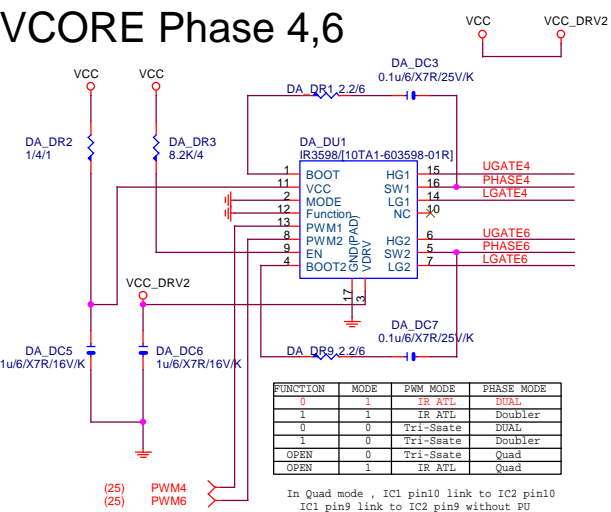
線路圖名稱	BIOS選項
Vcore	CPU Vcore
CPU_VTT	CPU Termination
CPU_VAXG	CPU Graphic Core
VCC1_8_PCH	CPU PLL
VCC1_05_PCH	PCH core
3VDUAL	3VDUAL
DDR15V	DRAM voltage
DDRVTT	DRAM Terminatio
VREF_CA_A/VREF_CA_B	DRAM Address Ref
VREF_DQ_A/VREF_DQ_B	DRAM Data Ref

散熱模組料號：

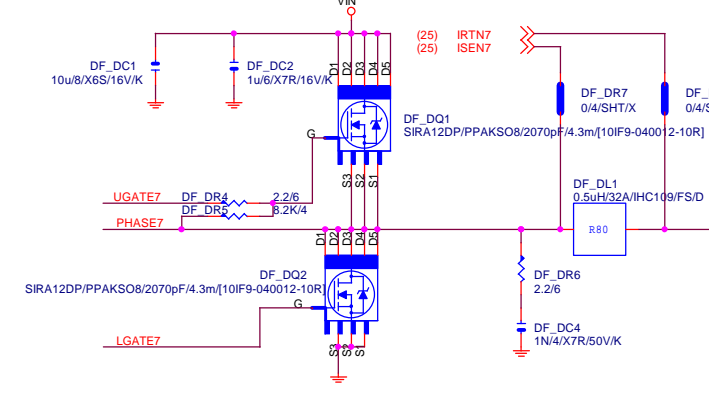
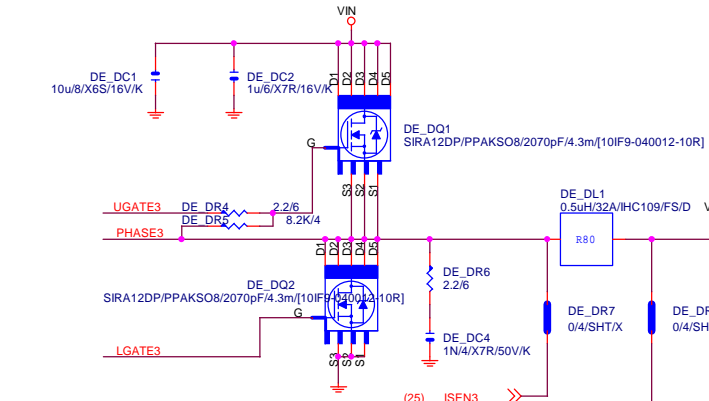
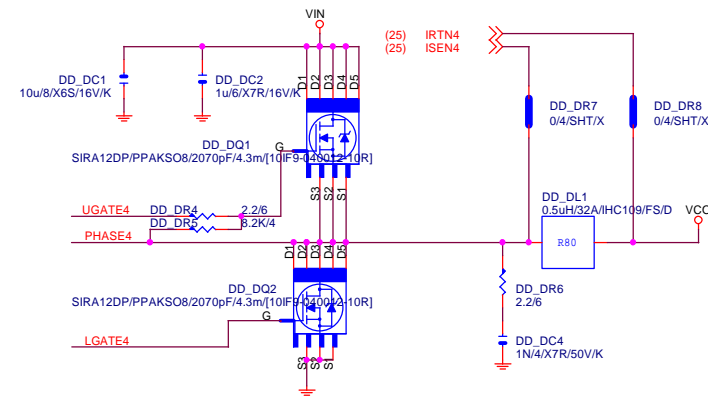
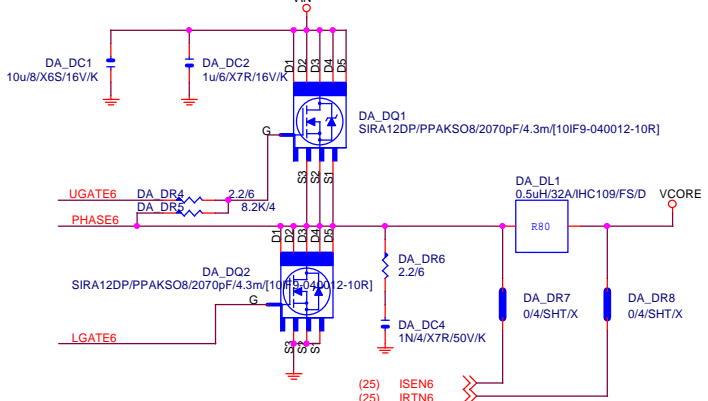
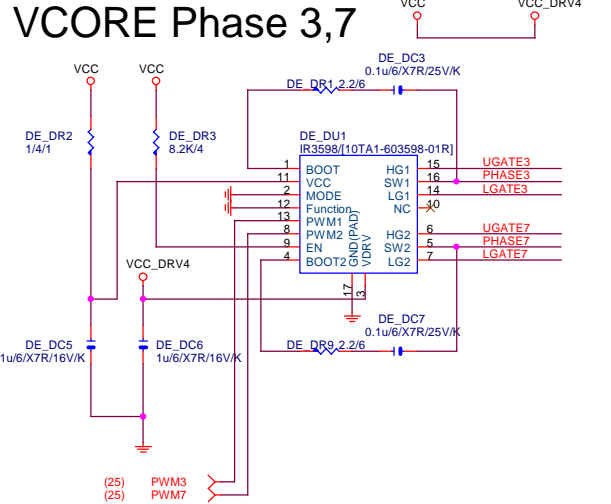
Z77-D3H :  
PCH :  
12SP2-S05511-01R/02R/03R  
MOSFET :  
12SP2-S08924-01R/02R/03R

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8720
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	PCH
SYS FAN	FANPWM2	N/A	FANIO2	IT8720
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	PCH
PWR FAN	N/A	N/A	FANIO3	IT8720
			ICH_FAN_TACH2	PCH

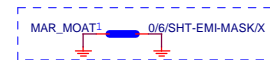
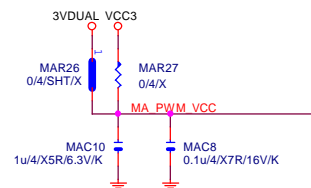
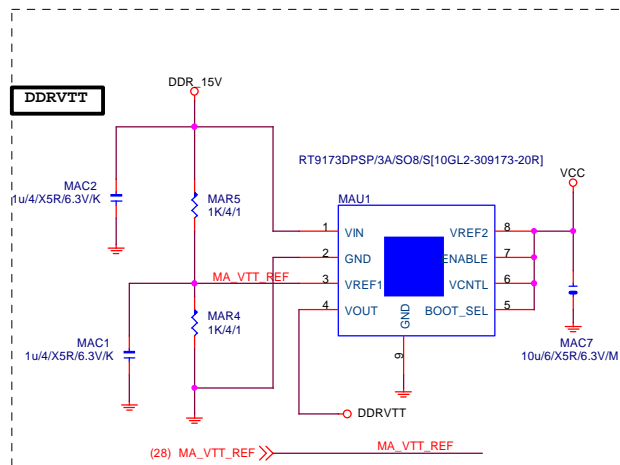
VCORE Phase 4,6



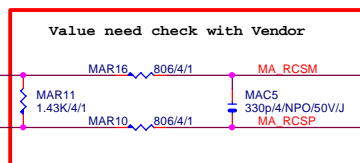
VCORE Phase 3,7





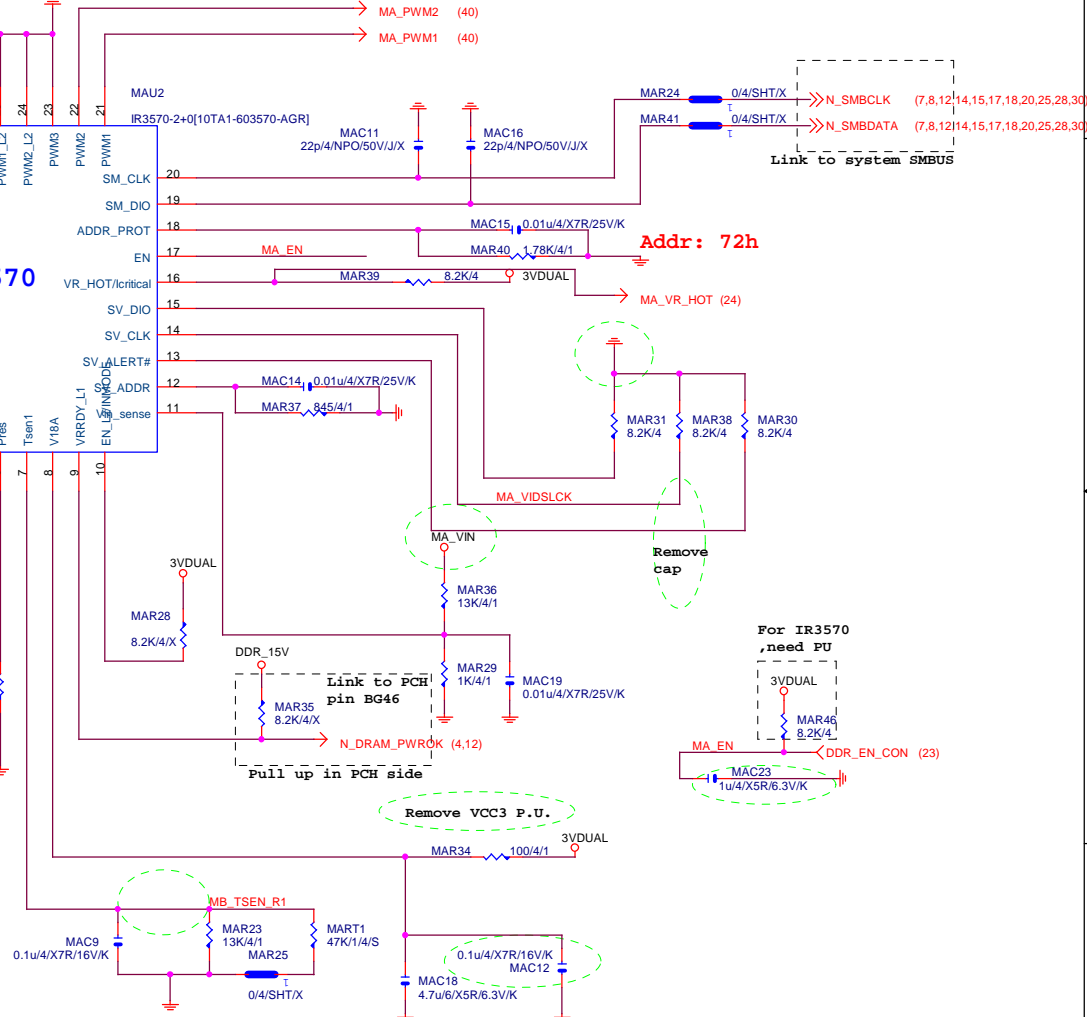
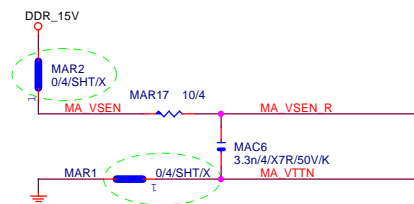
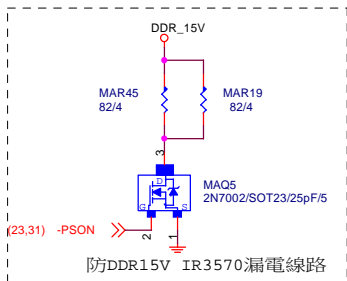


IR3570



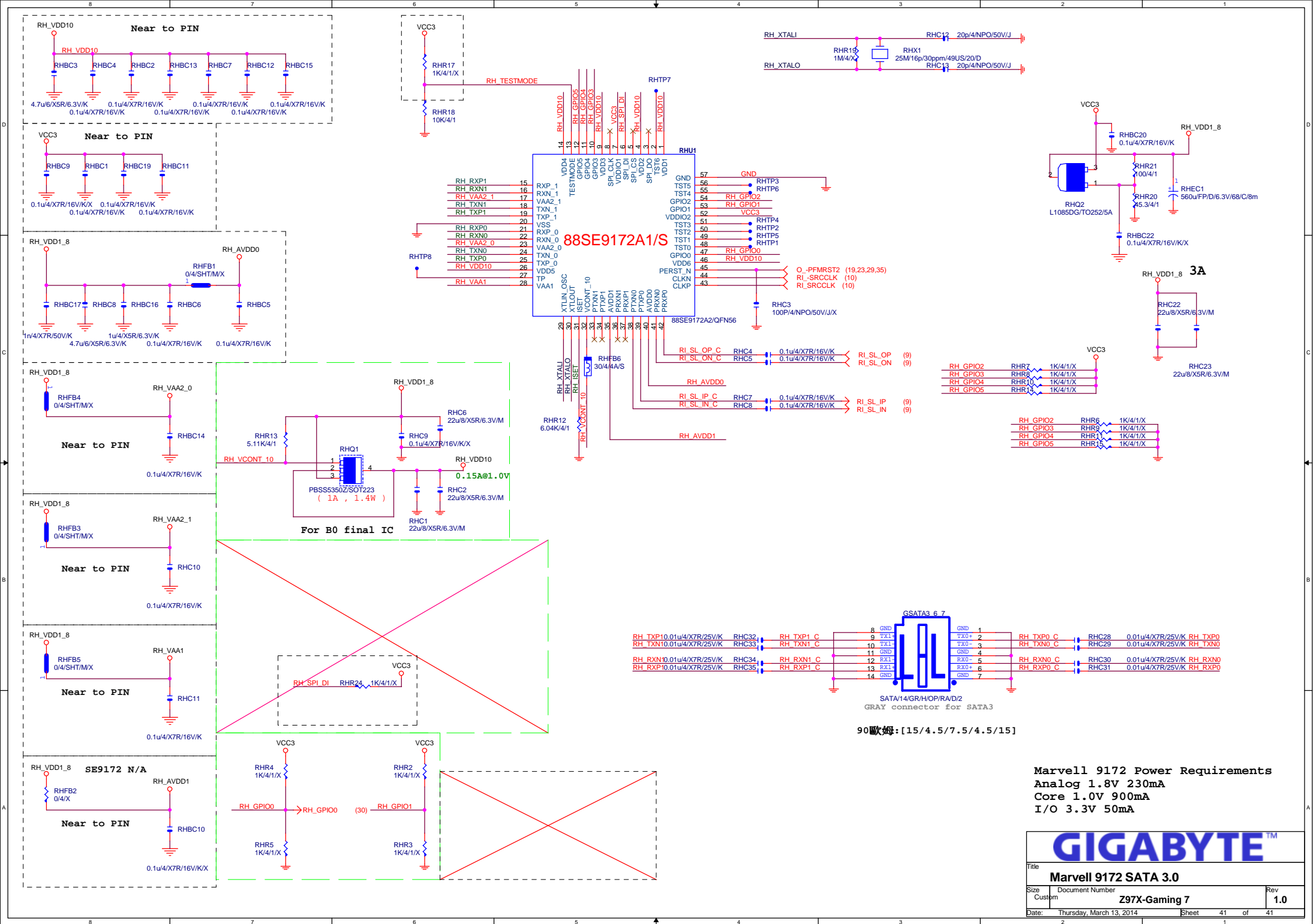
Close to DDR output inductor

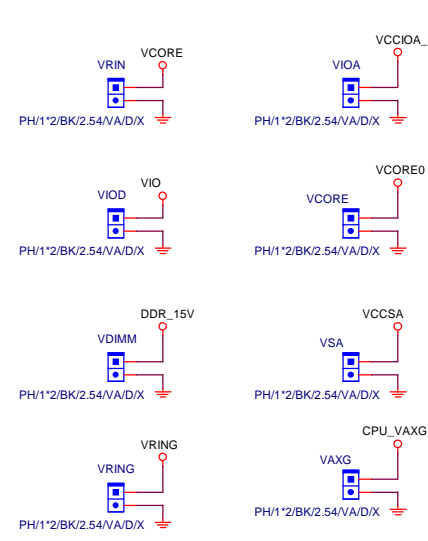
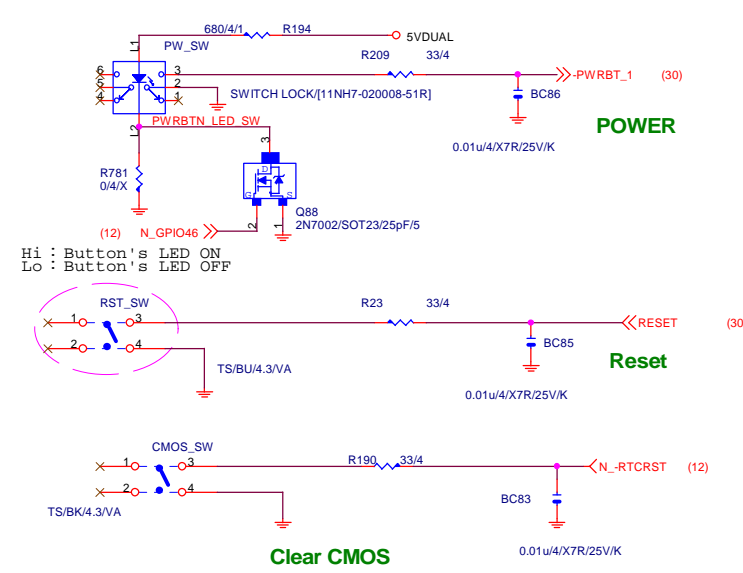
should be routed as differential pair, 7mil width, 8mil spacing



GIGABYTE™			
Title			
DDR POWER IR3570			
Size	Document Number	Rev	
Custpm	Z97X-Gaming 7	1.0	
Date:	Thursday, March 13, 2014	Sheet	39 of 41







80 PORT

